

Efficient Quantum Simulations of Devices Based on 2D Materials Including Vertical Heterojunctions

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Abstract—We present a modelling framework that enables efficient exploration of the electrical performance of devices based on 2D material vertical heterojunctions. Electronic structure data from density functional theory (DFT) simulations is used to extract parameters for k.p Hamiltonians. Material models are then employed in device simulations based on non-equilibrium Green's functions (NEGF) for a quantum-mechanical description of charge transport. Electron-phonon scattering is included in order to account for dissipative phenomena as well as phonon-assisted interlayer charge transport. We demonstrate our methodology with an application to a Dirac-source field-effect transistor (DS-FET) design based on a monolayer molybdenum disulfide channel (ML-MoS₂) with a graphene contact.

Index Terms—NEGF, cold source, phonon scattering

I. INTRODUCTION

Devices based on materials with reduced dimensionality are attractive candidates for post-silicon technologies due to their potential for increased packing density in integrated circuits. The layered nature of 2D materials represents an advantage for both mitigating variability issues, and ensuring excellent electrostatic control in ultra-scaled devices [1]. Over the past decades a wide range of 2D materials have been investigated for applications in optoelectronic devices, with several candidates exhibiting favorable carrier mobilities down to atomic thicknesses. Furthermore, their potential for device design is significantly expanded through to their remarkable ability to integrate with each other. In recent years, a number of device designs based on vertical heterojunctions between 2D materials have opened new possibilities for contact design, tunnelling junctions, and even novel switching mechanisms [2], [3]. The large number of potential material combinations as well as significant challenges posed by associated process engineering call for guidance from simulation techniques to efficiently screen device designs.

Simulation studies performed on devices based of 2D materials suggest formalisms beyond standard technology computer-aided design (TCAD) tools are required to accurately predict their electrical performance [4]. In this work, we present a TCAD framework that enables a computationally efficient workflow for rapidly evaluating the predicted performance of devices based on 2D materials including vertical heterojunctions (VHJ). Our methodology focuses on capturing

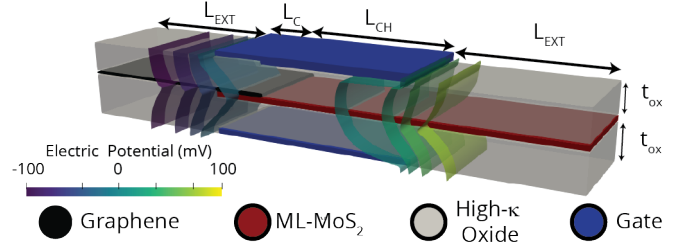


Fig. 1. Schematic of the double-gate device geometry studied. Top and bottom oxides are high- κ ($\epsilon_r = 25$) with thickness $t_{ox} = 3$ nm. Source and drain extensions of length $L_{EXT} = 15$ nm are explicitly included in the self-consistent simulation loop. Top and bottom gates span the length of the ML-MoS₂ channel $L_{CH} = 15$ nm, and the length of the vertical contact $L_C = 5$ nm. Periodic boundary conditions are applied along the width direction. Overlaid contour surfaces correspond to electric potential for $V_{GS} \approx V_{Th}$.

relevant quantum mechanical effects by employing advanced formalisms while retaining high computational efficiency. In the following, we present a case study on a cold source device design based on a heterojunction between graphene and ML-MoS₂. We demonstrate our implementation's ability to accurately describe quantum transport phenomena previously reported using computationally demanding atomistic simulations, at a fraction of the computational cost and without the need for specialised hardware. We additionally include the effects of electron-phonon scattering and discuss its impact on the switching characteristics of DS-FETs.

II. SIMULATION METHODOLOGY

The electrical properties of the double-gate device design depicted in fig. 1 are simulated using the \mathcal{Q}^* software package [5]. The DS-FET studied in this work is based on a graphene contact vertically coupled to ML-MoS₂, which comprises the channel and drain extension. Two layers of high- κ oxide ($\epsilon_r = 25$) with $t_{ox} = 3$ nm thickness insulate active materials from top/bottom gate electrodes. The gated region extends the length of both the ML-MoS₂ channel ($L_{CH} = 15$ nm) and the VHJ ($L_C = 5$ nm). Degenerately doped regions are induced along lead extensions to form carrier reservoirs on either side of the channel. Lead extension regions with lengths $L_{EXT} = 15$ nm are included in the explicit simulation domain. The ML-MoS₂ drain extension is doped n-type, while the

graphene source extension is doped p-type. Chemical doping is modeled using compensating charge densities homogeneously distributed along respective active material layers. Periodic boundary conditions are applied along the width direction to simulate infinitely wide devices.

The electrostatics and carrier transport equations are solved self-consistently in a Poisson-Schrödinger-NEGF loop within a coupled mode-space approach [6]. Relative permittivities of $\epsilon_r^{\text{Gph}} = 6.9$ and $\epsilon_r^{\text{MoS}_2} = 15.5$ are used within active materials when computing electrostatics [7], [8]. A recursive algorithm is used for efficient calculation of the Green's function when modeling charge transport [9]. The method includes explicit description of quantum effects in the active region such as source-drain tunneling and geometric scattering. The Q^* software package explicitly treats elastic and inelastic phonon scattering within the self-consistent Born approximation [10]. In this study we have used phonon deformation potentials for ML-MoS₂ and graphene reported in refs. [11], [12].

We model the electronic structure of each material in the vicinity of the K, K' valleys using $\vec{k} \cdot \vec{p}$ Hamiltonians defined as [13], [14]

$$\begin{aligned} \hat{H}_{\text{Gph}} &= \hbar v_F \begin{pmatrix} 0 & k_- \\ k_+ & 0 \end{pmatrix} \\ \hat{H}_{\text{MoS}_2} &= \begin{pmatrix} \epsilon_{vb} + \alpha k^2 & \gamma k_- \\ \gamma k_+ & \epsilon_{cb} + \beta k^2 \end{pmatrix}, \end{aligned} \quad (1)$$

where the energies of the valence and conduction bands in ML-MoS₂ are referred to the Dirac point in graphene, \hbar is Planck's constant, the Fermi velocity v_F the γ parameter describe the isotropic dispersion around band edges, α/β break electron-hole symmetry, and $k_{\pm} = k_x \pm i k_y$. The interlayer interaction along the VHJ is modeled as

$$\hat{H}_{int} = \begin{pmatrix} t_v & t_i \\ t_i & t_c \end{pmatrix}. \quad (2)$$

A fitting module integrated into the Q^* software package is employed to extract all Hamiltonian parameters using electronic structure data from ab-initio simulations. Reference band structure data have been generated by DFT simulations using ultra-soft pseudopotentials as implemented in QUANTUM ESPRESSO [15], [16]. The non-local exchange-correlation functional vdW-DF3 is used in order to describe van der Waals interactions between graphene and ML-MoS₂ [17]. The band offsets of ML-MoS₂ and the elements of \hat{H}_{int} are computed using a supercell including 5×5 unit cells of graphene and 4×4 unit cells of ML-MoS₂, where commensurability is enforced by applying 3.2% biaxial tensile strain to graphene.

III. RESULTS

Table I lists the continuum Hamiltonian parameters extracted from DFT data. Parameters for graphene and ML-MoS₂ have been extracted from simulations with isolated material layers. Figure 2 shows comparisons between band dispersions

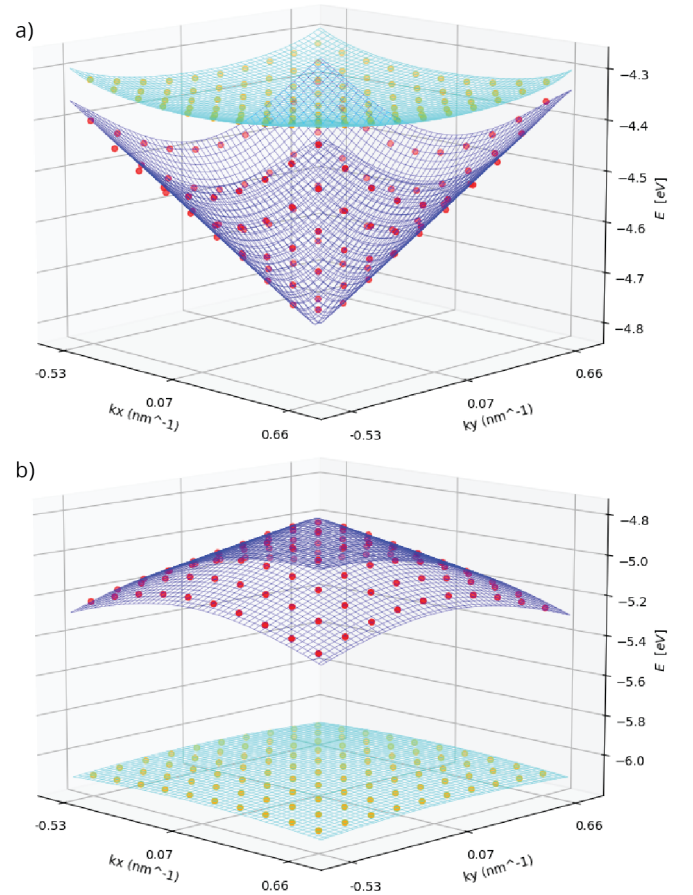


Fig. 2. Band structure for a Graphene/ML-MoS₂ vertical heterojunction near the Fermi level and around the K valley. Band edges for the a) conduction band and b) valence band computed with DFT (scatter plots), and with the continuum Hamiltonian employed for device simulations (mesh plots).

around the Fermi level of a graphene/ML-MoS₂ VHJ near the K valleys computed ab-initio, and with our fitted $\vec{k} \cdot \vec{p}$ model. Satisfactory agreement is achieved, with an l^2 -norm of the fitting error of 3×10^{-4} eV, and maximum deviations around 15 meV found at points furthest from band extrema. The band alignment obtained from the VHJ simulation results in a barrier height of $\Phi_B = 0.428$ eV between graphene's Dirac point (DP) and the conduction band minimum (CBM) in ML-MoS₂. The tensile strain applied to graphene for the VHJ supercell results in a reduction of its Fermi velocity

TABLE I
HAMILTONIAN PARAMETERS FITTED FROM AB-INITIO SIMULATIONS.

	Parameter	Value
Graphene	v_F	8.3×10^5 m/s
	t_v	0.030 eV
	t_c	0.013 eV
Interlayer	t_i	-0.014 eV
	α	0.593 eV/Å ²
	β	1.264 eV/Å ²
ML-MoS ₂	γ	3.66 eV/Å

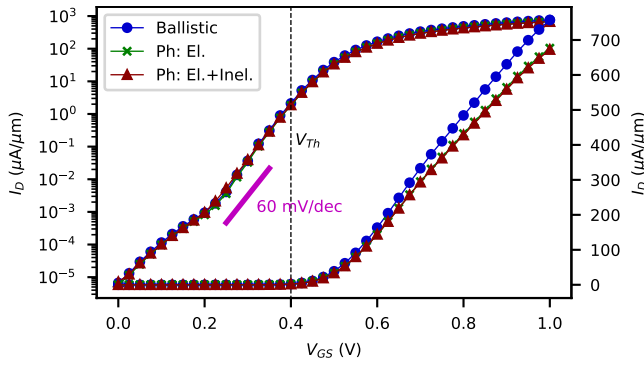


Fig. 3. Current-voltage characteristics simulated in the ballistic regime, with elastic phonon scattering, and with both elastic and inelastic phonon scattering. A threshold voltage of $V_{Th} = 0.4$ V has been extracted using the linear extrapolation method. Temperature is 300K.

of approximately 7.9%. No other significant deviations from the electronic structure of isolated layers were observed. The magnitude of Hamiltonian elements describing interlayer coupling is found to be similar to those reported for bilayer MoS₂, in agreement with previous literature [18], [19].

Figure 3 shows the simulated transfer characteristics at a source-drain bias of $V_{DS} = 0.5$ V in the ballistic regime, when including inelastic phonon scattering, and when including the effects of both inelastic and elastic phonon scattering. The reduction in ON-state currents induced by phonon scattering increases with gate bias, up to around 8% at $V_{GS} = 1$ V. This reduction may be attributed to inelastic phonon scattering, given the negligible difference between transfer characteristics across both phonon models for $V_{GS} > V_{Th}$.

The impact of phonon scattering on states below threshold voltage is illustrated in fig. 4. In all cases, the subthreshold swing (SS) drops below the thermodynamic limit around $V_{GS} = 0.3$ V. Figure 5 shows the corresponding local density of states (LDoS) and spectral current in the ballistic approximation, where we can observe a minimum SS is achieved when the gate action swings the channel CBM below the graphene source's DP, in line with expected DS-FET behavior.

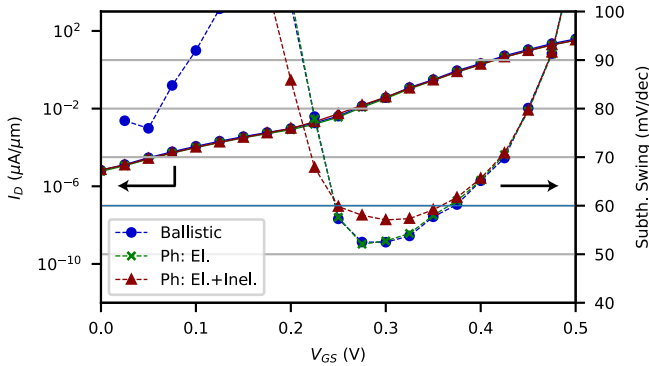


Fig. 4. Current and subthreshold swing versus gate bias in the ballistic limit, with elastic phonon scattering, and with both elastic and inelastic phonon scattering.

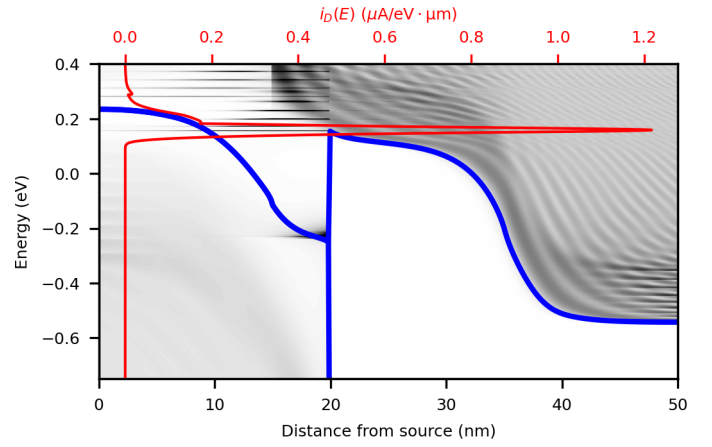


Fig. 5. Local density of states and spectral current in the ballistic approximation for $V_{GS} = 0.3$ V. Darker regions indicate larger density of states.

The energy dependence of the source density of states (DoS) modulates the rate at which electrons in graphene are able to overcome the barrier along the VHJ and into ML-MoS₂. As the gate bias increases and the channel CBM drops below the source DP, a decrease in source DoS with increasing energy results in sub-60 mV/dec switching [19]. The *sharpness* of the source DoS energy profile thus impacts the minimum SS that may be achieved. Since the source DoS is characterized by graphene's Fermi velocity, the SS dependence with gate bias is found to depend significantly on the value used in our simulations. Strategies that modify the DoS in graphene, such as strain engineering, may improve switching in DS-FET designs.

Although no significant variations in OFF currents are observed in fig. 4 when including phonon scattering, the introduction of inelastic scattering processes modifies the device switching characteristics. The impact of inelastic phonon scattering on current results in a smoother SS dependence with gate bias with respect to the ballistic / elastic scattering

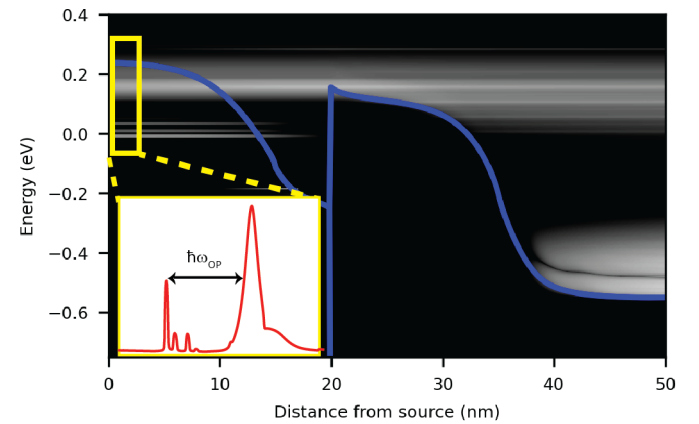


Fig. 6. Local spectral current when including elastic and inelastic phonon scattering for $V_{GS} = 0.3$ V. Brighter regions indicate larger spectral current. Inset illustrates peaks in the source side correspond to phonon absorption processes.

cases. The additional transport pathways enabled by inelastic scattering allow electrons in graphene with energies below the CBM to tunnel into channel, as illustrated in fig. 6. Absorption of optical phonons enable electrons in graphene with energies up to 0.2 eV below the CBM to contribute to current. The rate of carriers able to overcome the channel barrier is thus not solely determined by the source DoS at energies around the CBM. This results in an effective smearing of the source DoS, reducing the effectiveness of graphene's sharp DoS profile in achieving sub-60 mV/dec switching.

IV. CONCLUSION

We have presented an application of a framework that combines atomistic and continuum models to enable computationally efficient device simulations using state-of-the-art quantum formalisms. The capability of modeling devices based on 2D materials including vertical heterojunctions was illustrated by studying the electrical characteristics of a graphene/ML-MoS₂ DS-FET design. The electronic structure of graphene, ML-MoS₂, and associated heterojunction were modeled using $\vec{k} \cdot \vec{p}$ Hamiltonians parametrized from DFT simulations. Our description was found to reproduce ballistic transport characteristics previously reported using atomistic simulation methods. The effects of phonon scattering were assessed and found to degrade ON currents and significantly impact device switching characteristics. Inelastic phonon scattering was determined to be main mechanism behind ON current reduction. The bias dependence of the subthreshold swing was found to be smoothed by electron-phonon interactions, degrading the sub-60 mV/dec feature characteristic of DS-FETs. Current spectrum analysis reveals inelastic scattering in graphene to be the primary mechanism behind this effect. The methodology employed in this work aims to bridge the gap between TCAD and ab-initio simulations in order to minimize the use of ad-hoc parameters in an effort to provide enhanced physical insight. Facilitating rapid and accurate evaluation of device designs based on 2D materials and their combinations will be key in guiding experimental efforts on the road to achieving competitive post-silicon technologies.

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