

A Numerically Efficient Method for the Hydrodynamic Density-Gradient Model

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Abstract—We propose a quantum transport model that is a hydrodynamic extension of the density-gradient model. The governing equations are derived from the moments of the Wigner distribution function and their forms are suitable for the conventional device simulation program. The model is discretized by the control volume method with nonlinear discretizations for the electron and energy flux equations. We also developed a boundary condition for the Si/SiO₂ interface that includes the electron wavefunction penetration into the oxide to obtain more accurate *C-V* characteristics. As an application, we studied a 25nm NMOSFET device. Compared with the semiclassical models, the new model predicts reduced gate capacitance about 20% and increased subthreshold slope and DIBL about 16% and 46% respectively. Compared with the density-gradient model, the on-current is increased up to 26% due to the nonlocal transport effect.

I. INTRODUCTION

As the semiconductor device size is scaled down aggressively, its characteristics are changed by the emerging quantum effect. Therefore, semiclassical transport models such as the drift-diffusion (DD) model [1] or the hydrodynamic (HD) model [2] cannot be applied directly to the simulation of nanoscale devices. To overcome this limit of the semiclassical models, various quantum corrected transport models have been proposed [3]–[5]. Among them, the density-gradient (DG) model extends the DD model by including an effective quantum potential in the drift term and it has become one of the successful models that can simulate the quantum effect [3], [6]–[11].

As the magnitude and variation of the electric field increase in the modern devices, hydrodynamic extension of the DG model becomes necessary to include the nonlocal transport effect in addition to the quantum effect.

In the present paper, we will introduce a quantum transport model called *hydrodynamic density-gradient (HDG) model* that extends the DG model by including the energy balance equation. We will describe its governing equations, derivation, boundary conditions, and implementation in detail. As an application, we will discuss the simulation results of a 25nm NMOSFET device.

II. BASIC MODELS

For simplicity, we consider only the case of electrons. The DG model introduces a nonlocal quantum potential, which depends on the gradient of the electron density as [6]

$$\psi_{\text{qn}} = 2b_n \frac{\nabla^2 \sqrt{n}}{\sqrt{n}}, \quad (1)$$

where n is the electron density and $b_n = \hbar^2 / (12qm_n^{\text{DG}})$ ($m_n^{\text{DG}} = 0.258m_0$ for silicon [7]) is the linear gradient coefficient that represents the strength of the quantum correction.

A. Governing Equations

In the steady-state condition, our HDG model solves the following set of governing equations:

$$-\nabla \cdot (\epsilon \nabla \psi) - q(p - n + N_D^+ - N_A^-) = 0, \quad (2)$$

$$\nabla \cdot \mathbf{F}_n + U = 0, \quad (3)$$

$$\nabla \cdot (b_n \nabla \sqrt{n}) - \frac{\sqrt{n}}{2} \psi_{\text{qn}} = 0, \quad (4)$$

and

$$\nabla \cdot \mathbf{S}_n - q \nabla \left(\psi + \frac{5}{2} \psi_{\text{qn}} \right) \cdot \mathbf{F}_n + U w_n + n \frac{w_n - w_0}{\tau_{\text{wn}}} = 0 \quad (5)$$

with auxiliary equations for the electron and energy fluxes:

$$\mathbf{F}_n = \mu_n n \nabla (\psi + \psi_{\text{qn}}) - \mu_n \nabla \left(\frac{k_B T_n}{q} n \right) \quad (6)$$

and

$$\mathbf{S}_n = -\kappa_n \nabla T_n + (w_n + k_B T_n) \mathbf{F}_n, \quad (7)$$

where (2) is the Poisson equation, (3) is the continuity equation, (4) is the quantum potential equation, and (5) is the energy-balance equation. In the above equations, ϵ is the electric permittivity, ψ is the electrostatic potential, p is the hole density, T_n is the electron temperature, N_D^+ and N_A^- are the ionized donor and acceptor densities, U is the net recombination rate [8], $w_n = \frac{3}{2} k_B T_n$ is the average electron energy (kinetic energy is neglected), $w_0 = \frac{3}{2} k_B T_0$ is the electron energy in equilibrium, T_0 is the lattice temperature, τ_{wn} is the energy relaxation time, μ_n is the electron mobility, and $\kappa_n = (5/2 + c) \frac{k_B^2}{q} T_n \mu_n n$ ($c = -1.9$) is the thermal conductivity [2]. The unknown variables are ψ , ψ_{qn} , \sqrt{n} , and T_n for (2), (3), (4), and (5) respectively.

In the derivation of the governing equations from the quantum moment equations [5], we neglect the $\mathbf{v} \cdot \nabla \mathbf{v}$ term in the momentum balance equation and the kinetic energy term ($\frac{1}{2} m_n v^2$) in the energy balance equation. The relation as

$$\nabla (n b_n \nabla^2 \ln n) = n \nabla \left(2b_n \frac{\nabla^2 \sqrt{n}}{\sqrt{n}} \right) \quad (8)$$

is used to transform the quantum correction term from the temperature part to the external potential part in (5), (6),

and (7). Compared with [5], our formulation includes the heat flow term ($-\kappa_n \nabla T_n$) in the energy flux equation and the recombination term (Uw_n) in the continuity and energy balance equations. As a result, our HDG model exactly returns to the conventional HD model in the classical limit ($b_n \rightarrow 0$) and the DG model in thermal equilibrium ($T_n \rightarrow T_0$).

B. Boundary Conditions

There exist three kinds of boundaries in the simulation domain: electrodes, Si/SiO₂ interfaces, and artificial boundaries with the Neumann boundary conditions for fluxes [1].

We apply the Dirichlet boundary conditions at the electrodes by the following assumptions: the electrostatic potential is given as a bias condition, the charge neutrality condition holds, the quantum potential vanishes, and the electrons are in thermal equilibrium ($T_n = T_0$).

At the Si/SiO₂ interfaces, we can assume that the dielectric flux is continuous and the normal components of the electron and energy fluxes are zero for (2), (3), and (5). For (4), conventional DG model usually sets vanishingly small electron density at the interface by assuming infinite Si/SiO₂ potential barrier [9]. But this insulating boundary condition overestimates the quantum confinement effect since the actual potential barrier is finite and the electron wavefunction can penetrate into the oxide. So we propose a new boundary condition for (4).

The electron density penetrated into the oxide by x from the Si/SiO₂ interface can be approximated as

$$n(x) = n_0 \exp(-2x/x_{np}), \quad (9)$$

where n_0 is the electron density at the interface and

$$x_{np} = \frac{\hbar}{\sqrt{2m_{\text{nox}}\Phi_{\text{Bn}}}} = 1.7529\text{\AA} \quad (10)$$

is the characteristic penetration depth obtained from the WKB approximation. Here m_{nox} ($0.4m_0$) is the electron effective mass in the oxide and Φ_{Bn} (3.1eV) is the potential barrier height [12]. From (9), the outward normal component of the $b_{\text{nox}}\nabla\sqrt{n}$ at the interface can be written as

$$\mathbf{n} \cdot b_{\text{nox}}\nabla\sqrt{n} = -(b_{\text{nox}}/x_{np})\sqrt{n_0}, \quad (11)$$

where $b_{\text{nox}} = \hbar^2 / (12qm_{\text{nox}}^{\text{DG}})$ ($m_{\text{nox}}^{\text{DG}} = 0.14m_0$) is the linear gradient coefficient for the oxide region. This equation is used as the penetrating boundary condition for (4).

C. Physical Parameters

The simulation results largely depend on the electron mobility and the energy relaxation time. For the low field mobility (μ_{n0}) model, we apply a local mobility model that includes the lattice scattering, Coulomb scattering with screening effect, surface phonon scattering, and surface roughness scattering [13]. As for the temperature dependent mobility model, we apply the Caughey-Thomas expression with the effective field approach as [14]

$$\mu_n(T_n) = \frac{\mu_{n0}}{\left[1 + \left\{\frac{3k_B\mu_{n0}(T_n - T_0)}{2qv_{\text{satn}}^2\tau_{\text{wn}}}\right\}^\beta\right]^{1/\beta}}, \quad (12)$$

where $\beta = 2$ and v_{satn} is assumed to be 8×10^6 cm/sec in the inversion layer.

The energy relaxation time depends on the low field mobility and the electron temperature. An analytic expression for the energy relaxation time can be written as [15]

$$\tau_{\text{wn}} = \frac{3}{2} \frac{k_B\mu_{n0}}{qv_{\text{satn}}^2} \frac{T_n T_0}{T_n + T_0}. \quad (13)$$

Since our sample device has very high channel doping and thin gate oxide, the low field mobility is in the range from 100 to 150 cm²/V·sec. If we assume $\mu_{n0} = 130$ cm²/V·sec, the energy relaxation time is in the range from 0.04 to 0.08 psec. Based on this estimation, τ_{wn} is set to be 0.05 psec.

III. NUMERICAL METHODS

The governing equations are discretized by the control volume method. The flux terms in (2) and (4) are discretized by the central difference scheme. To discretize the electron and energy flux equations, we apply the nonlinear discretization scheme of the HD model [2] by replacing ψ with $\psi + \psi_{\text{qn}}$. The discretized form of the electron flux along the mesh line between the i th and j th nodes can be written as

$$F_n = -D_n \frac{\Delta T}{\Delta x \ln(T_j/T_i)} \left[B(\tilde{\Delta}) \frac{n_j}{T_j} - B(-\tilde{\Delta}) \frac{n_i}{T_i} \right], \quad (14)$$

where $\Delta x = x_j - x_i$, $\Delta T = T_j - T_i$, $B(x)$ is the Bernoulli function, and

$$\tilde{\Delta} = \frac{\ln(T_j/T_i)}{\Delta T} \left[\frac{q}{k_B} (\Delta\psi + \Delta\psi_{\text{qn}}) - 2\Delta T \right]. \quad (15)$$

Similarly, the discretized form of the energy flux equation can be written as

$$S_n = -(5/2 + c) \frac{k_B D_n}{\Delta x} \tilde{N} [B(\Phi)T_j - B(-\Phi)T_i], \quad (16)$$

where

$$\tilde{N} = \frac{\Delta T}{\ln(T_j/T_i)} \frac{n_j}{T_j} \frac{B(\tilde{\Delta})}{B(\tilde{\Phi})}, \quad (17)$$

$$\tilde{\Phi} = \frac{\ln(T_j/T_i)}{\Delta T} \left[\frac{q}{k_B} (\Delta\psi + \Delta\psi_{\text{qn}}) - \Delta T \right] - \ln \frac{n_j}{n_i}, \quad (18)$$

and

$$\Phi = \frac{5/2}{5/2 + c} \tilde{\Phi}. \quad (19)$$

The discretized coupled equations are solved by the full Newton method. We found that either iterative or direct matrix solver can be applied. To implement the model, we developed a device simulator called *NANOCAD*.

IV. SIMULATION RESULTS AND DISCUSSION

We simulated a bulk NMOSFET whose effective channel length is 25 nm and physical oxide thickness is 15 Å [16]. The gate material is uniformly doped (5×10^{20} /cm³) n^+ polysilicon and the channel doping profile is based on the super-halo [17] to prevent the short-channel effect. Fig. 1 shows the doping profile of the device. From this figure, we can find that the source/drain junction depth is 25 nm and the metallurgical channel length is 22 nm [18].

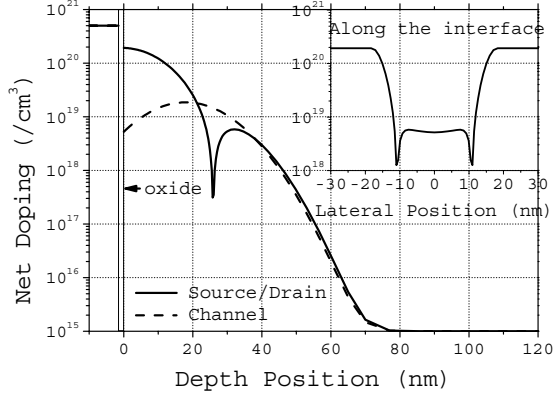


Fig. 1. Doping profile of the 25nm NMOSFET under consideration.

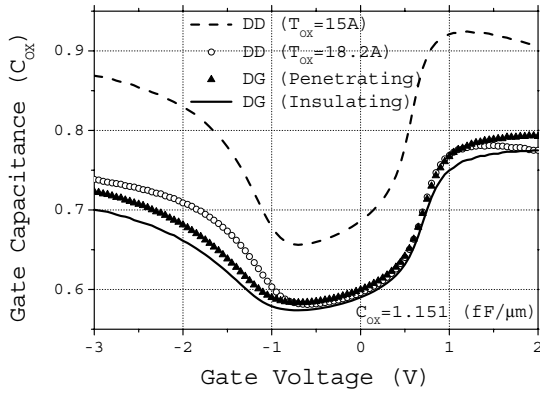


Fig. 2. Comparison of the C_G - V_G characteristics between the DD model ($t_{ox} = 15\text{\AA}$ and $t_{ox} = 18.2\text{\AA}$) and the DG model (the penetrating boundary condition and the insulating boundary condition).

We first studied the influence of the quantum effect on the gate capacitance. Fig. 2 shows the C_G - V_G characteristics of the device. The quantum effect reduces the gate capacitance about 20% and increases the effective oxide thickness about 3.2\AA . Fig. 3 shows the electrostatic, quantum, and effective potentials along the 1D cut line through the center of the channel. The quantum potential smoothes the potential variation near the Si/SiO₂ interface. The interface boundary condition also changes the gate capacitance. The penetrating boundary condition increases the gate capacitance slightly compared with the insulating boundary condition. To see the influence of the interface boundary condition, we plotted the electron density in the inversion layer in Fig. 4. As a reference, we calculated the electron density in the oxide as well as in the inversion layer by treating the oxide region as a wide bandgap semiconductor (solid line). The electron density calculated from the penetrating boundary condition follows the solid line very well, while the insulating boundary condition shifts the electron density away from the interface about 1\AA .

In Fig. 5, we compared the I_D - V_G characteristics between the HD and HDG models. As we include the quantum effect, the subthreshold slope and DIBL are increased from 93 mV/dec to 108 mV/dec and from 99 mV to 145 mV

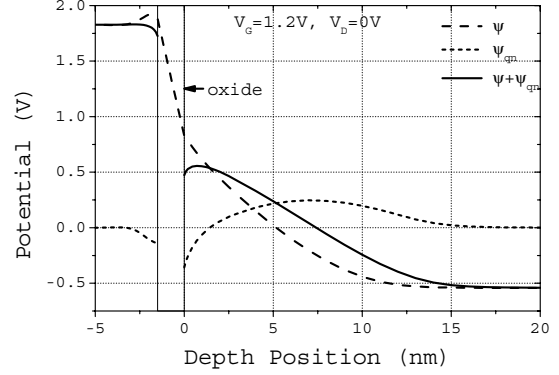


Fig. 3. Comparison of the electrostatic, quantum, and effective potentials along the 1D cut line through the center of the channel.

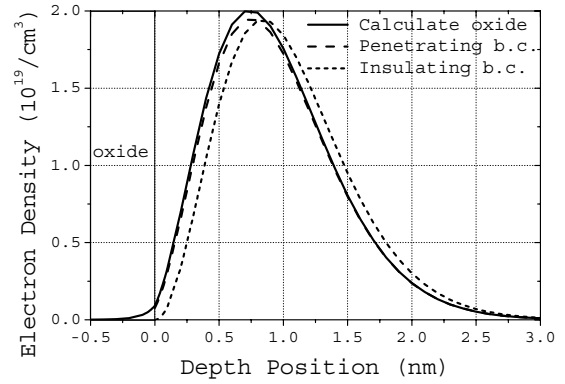


Fig. 4. Comparison of the electron density in the inversion layer.

respectively. Since the quantum confinement effect increases the effective oxide thickness, the gate electrode cannot control the channel charge effectively, which results in the degradation of the short-channel effect.

Fig. 6 shows the I_D - V_D characteristics calculated from the DG and HDG models. The HDG model predicts higher on-current up to 26%, which can be explained by the nonlocal transport effect. Fig. 7 shows the electron temperature in the channel when the bias conditions are in the linear and saturation regions. The electron temperature does not change appreciably until the channel position is about -10 nm , which means that the mobility at the source side of the channel remains high. Fig. 8 shows the average electron velocity in the channel calculated from the DG and HDG models. The HDG model predicts larger electron velocity in the channel, which results in larger on-current. The figure also shows the velocity overshoot effect in the drain side of the channel.

V. CONCLUSION

We obtained the HDG model from the quantum moment equations. The HDG model can be viewed as a hydrodynamic extension of the DG model and it returns to the HD model in the classical limit and the DG model in thermal equilibrium. We also improved the boundary condition for the Si/SiO₂ interface by including the electron wavefunction penetration

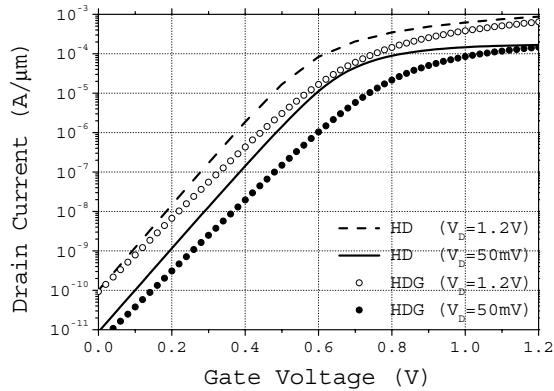


Fig. 5. Comparison of the I_D - V_G characteristics of the MOSFET between the HD and HDG models when V_D is 50 mV and 1.2 V.

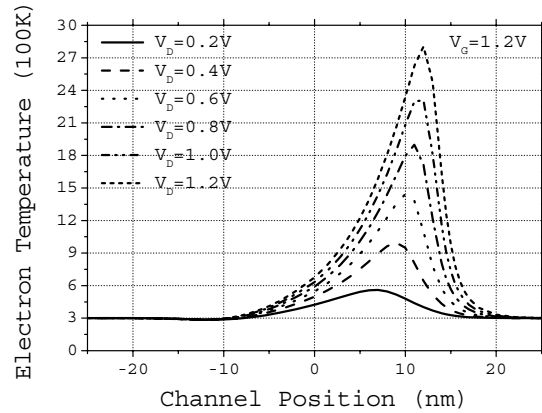


Fig. 7. Electron temperature in the channel when V_G is 1.2 V and V_D is 0.2, 0.4, 0.6, 0.8, 1.0, and 1.2 V.

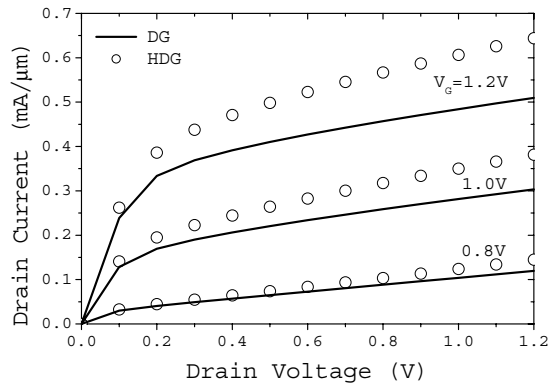


Fig. 6. Comparison of the I_D - V_D characteristics between the DG and HDG models when V_G is 0.8, 1.0, and 1.2 V.

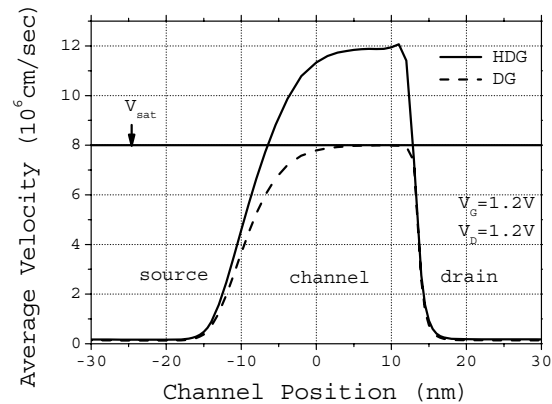


Fig. 8. Comparison of the average electron velocity in the channel between the DG and HDG models when V_G and V_D are 1.2 V.

effect. We applied our model to the 25nm NMOSFET device, which is in the limit of the bulk MOSFET. The simulation results show that the quantum effect and the nonlocal transport effect can change the device characteristics about 20-30%. The simulation time is increased about 2-4 times compared with the semiclassical models, which is tolerable.

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