

Device Simulation of Negative-Capacitance Field-Effect Transistors With a Ferroelectric Gate Insulator

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Abstract—We consider the method to simulate negative-capacitance field-effect transistors having a ferroelectric film as a gate insulator in the framework applicable to technology computer-aided design device simulators and propose a method with complete applicability. In the method, the behavior of the polarization in ferroelectrics is described by the Landau–Khalatnikov equation and it is solved simultaneously with the Poisson equation to obtain the distribution of the polarization and electrostatic potential. Also, the proposed method enables the device simulators to take into account a factor related to forming domain structures of the polarization.

Index Terms—TCAD, device simulation, negative capacitance, ferroelectrics

I. INTRODUCTION

Integrated circuits have been strongly required to reduce their power consumption for portable, wearable, implantable, and wireless sensor network applications. An effective way is to lower the operating voltage. To this purpose, constituent transistors should be switched on and off within a narrow input voltage range. Negative-capacitance (NC) field-effect transistors (FETs) exploiting the NC state of ferroelectric materials have been proposed as such steep switching transistors [1]. The ferroelectric film inserted in the gate stack as a gate insulator reduces the voltage applied to the underlying structure at a low gate voltage V_G , while enhances it at a high V_G . Owing to this V_G modulation, the structure beneath the ferroelectric film experiences a larger variation in the applied voltage than a given variation in V_G and consequently the drain current steeply increases or decreases. NC FETs have recently been studied intensively and their steep switching has already been reported by a number of research groups [2]–[4]. To support and promote such studies, the establishment of simulation method for NC FETs is urgently needed.

In the previous representative method [5]–[9], which implicitly targets NC FETs having a metal film beneath a ferroelectric film, the behavior of the ferroelectric capacitor sandwiched between the internal metal film and gate electrode and that of the remaining transistor structure are simulated separately and then the results obtained for the transistor are corrected by the voltage across the capacitor. Although this method uses the assumption that the polarization is uniform in ferroelectrics, several methods taking into account the distribution of the polarization have recently been proposed [10], [11]. Device simulator in technology computer-aided

design (TCAD) system is a de facto standard for the simulation of electronic devices' behavior. Although in the former method the simulator is usually used for the simulation of the transistor part [5]–[8], to fully incorporate the method into the simulator seems to be difficult due to the differences in the simulation framework and algorithm, which applies to the latter method.

We have developed our own device simulator, Impulse TCAD [12], and realized the simulation of NC FETs, thereby revealing their interesting features such as reverse drain-induced barrier lowering, negative drain conductance, and fringing field effect [13]–[15]. In this work, we consider the way for TCAD device simulators to properly simulate NC FETs' behavior. In the simulation of NC FETs, the polarization field of ferroelectrics is described by the Poisson and Landau–Khalatnikov equations. We discuss the discretization methods of these equations and propose a method completely applicable to the device simulators. The method also has high extensibility, which allows us to easily introduce ferroelectrics' feature of polarization vectors to align with each other into the simulation.

II. THEORY

A. Device Simulation

In a typical device simulation, the electrostatic potential distribution $\psi(\mathbf{r})$ in a target device is described by the following Poisson equation,

$$\nabla \cdot [\varepsilon(\mathbf{r})\mathbf{E}(\mathbf{r})] - \rho(\mathbf{r}) = 0, \quad (1)$$

where $\varepsilon(\mathbf{r})$ and $\rho(\mathbf{r})$ are the permittivity and charge density at the position \mathbf{r} , respectively, and $\mathbf{E}(\mathbf{r})$ is the electric field and is given by $\mathbf{E}(\mathbf{r}) = -\nabla\psi(\mathbf{r})$. Also, the concentration distributions of electrons and holes are governed by the continuity equations of their currents. As the first step to solve these three equations, a device simulator discretizes them using a finite volume scheme. In the scheme, the target device is divided into finite volumes referred to as control volumes and associated with each of the nodes of a computational mesh. In this study, the control volume of a node is assumed to be bounded by planes, each of which is perpendicular to the edge between the node and an adjacent node and is passing through the midpoint of the edge, as shown in Fig. 1. Representing the values of parameters and variables in a control volume by those at its owner node and integrating an equation over

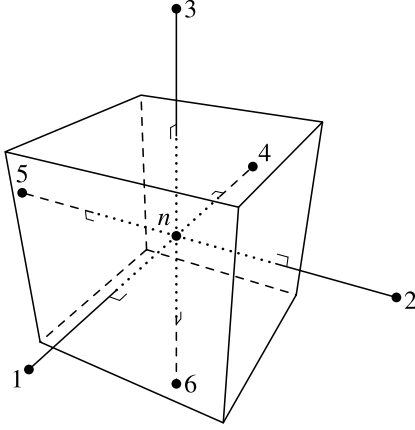


Fig. 1. Schematic view of the control volume of a node n , each of whose faces is perpendicular to the edge between the node and an adjacent node and is passing through the midpoint of the edge.

the control volume, we can derive the discretized form of the equation. For example, the discretized Poisson equation (1) for the n th node can be written as

$$\sum_m \varepsilon_n E_{nm} S_{nm} - \rho_n V_n = 0, \quad (2)$$

where the summation over m is taken for all adjacent nodes, V_n is the volume of the n th node's control volume, and S_{nm} is the area of the face of V_n intersecting with the edge from the n th to the m th node \mathbf{r}_{nm} . Also, E_{nm} denotes the component of the electric field on S_{nm} along \mathbf{r}_{nm} and can be approximated as $E_{nm} = -(\psi_m - \psi_n)/\ell_{nm}$ with ℓ_{nm} being the length of \mathbf{r}_{nm} . In this way, the Poisson and two current continuity equations are rewritten as a lot of equations having the electrostatic potential and electron and hole concentrations at each nodes as variables, and then the device simulator solves them to determine the variables' values using the Newton–Raphson method. Concretely, when the equations are written together as $f(\mathbf{v}) = \mathbf{0}$, the difference between the very solution \mathbf{v}_∞ and its guess value \mathbf{v}_0 can be estimated from the following equation,

$$\frac{\partial f(\mathbf{v}_0)}{\partial \mathbf{v}} \Delta \mathbf{v} = -f(\mathbf{v}_0). \quad (3)$$

The sum of \mathbf{v}_0 and the estimated difference value $\Delta \mathbf{v}$ is expected to approach \mathbf{v}_∞ compared to \mathbf{v}_0 . Thus, repeatedly solving this equation while updating \mathbf{v}_0 , we can obtain a \mathbf{v} 's value sufficiently close to \mathbf{v}_∞ .

B. Ferroelectrics

We described the behavior of the polarization $\mathbf{P} = (P_x, P_y, P_z)$ in a ferroelectric material using the Landau–Khalatnikov model [16],

$$\lambda \frac{\partial P_i}{\partial t} = -\frac{\partial G}{\partial P_i} \quad (i = x, y, z), \quad (4)$$

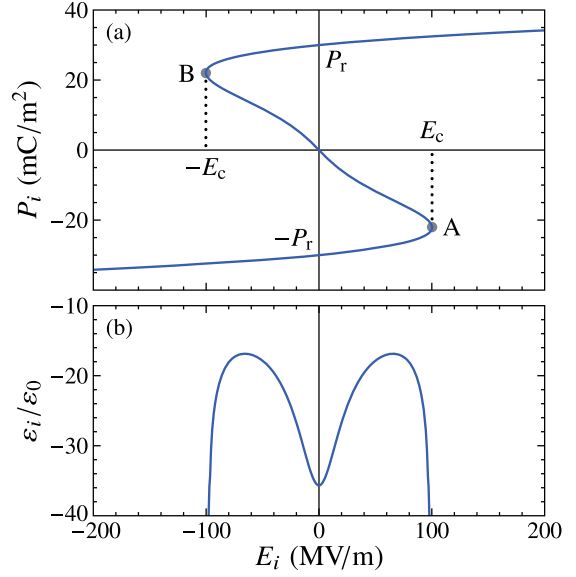


Fig. 2. (a) Polarization along each axis in the considered ferroelectric material plotted as a function of the electric field. NC states lie on the A–B line. (b) Permittivity normalized by that of vacuum and calculated for the NC states.

where λ is the material parameter related to the mobility of domain walls and G is the Gibbs free energy. In the Landau–Devonshire model [17], its density can be written as

$$g(\mathbf{r}) = \alpha(P_x^2 + P_y^2 + P_z^2) + \beta(P_x^4 + P_y^4 + P_z^4) + \gamma(P_x^6 + P_y^6 + P_z^6) - \mathbf{E} \cdot \mathbf{P}, \quad (5)$$

where α , β , and γ are the material parameters. In a steady state, (4) is rewritten as

$$2\alpha P_i + 4\beta P_i^3 + 6\gamma P_i^5 - E_i = 0 \quad (i = x, y, z). \quad (6)$$

Fig. 2(a) shows this relationship between \mathbf{P} and \mathbf{E} in the considered ferroelectric material, whose α , β , and γ were respectively set to -1.54×10^9 m/F, -2.65×10^{12} m⁵/FC², and 2.60×10^{15} m⁹/FC⁴ so that the remanent polarization P_r and coercive electric field E_c are about 3×10^{-3} C/m² and 100×10^6 V/m. In the figure, the ferroelectric material is in an NC state on the A–B line and its permittivity has a negative value, as shown in Fig. 2(b).

Solving (6) together with the Poisson equation (1), we can obtain $\psi(\mathbf{r})$ in the ferroelectric material. In doing so, to handle \mathbf{P} independently of \mathbf{E} , (1) was modified as follows:

$$\nabla \cdot [\varepsilon_0 \mathbf{E}(\mathbf{r}) + \mathbf{P}(\mathbf{r})] - \rho(\mathbf{r}) = 0, \quad (7)$$

where ε_0 is the permittivity of vacuum. In addition, the charge density $\rho(\mathbf{r})$ was set to 0.

III. RESULTS AND DISCUSSION

A. Decoupled Solution

The discretized form of (7) can be written as

$$\sum_m (\varepsilon_0 E_{nm} + P_{nm}) S_{nm} - \rho_n V_n = 0, \quad (8)$$

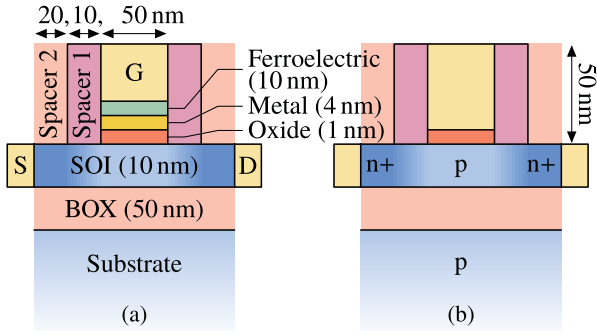


Fig. 3. (a) Schematic view of an NC FET. The regions labeled "G", "S", and "D" represent the gate, source, and drain electrodes, respectively. The thicknesses of the film components are shown in parentheses in their labels. (b) Schematic view of the corresponding MOS FET, which is obtained from the NC FET by replacing the ferroelectric and internal metal films with the gate electrode.

where P_{nm} is the component of the polarization on the control volume's face S_{nm} , P_{nm} , along the edge r_{nm} and is given by $P_{nm} = P_{nm} \cdot \mathbf{u}_{nm}$ with \mathbf{u}_{nm} being the unit vector of r_{nm} . The determination of P_{nm} is the key issue in the simulation of NC FETs. A straightforward way is to approximate the electric field on S_{nm} , \mathbf{E}_{nm} , as $\mathbf{E}_{nm} = E_{nm}\mathbf{u}_{nm}$ and then solve (6) for each component of P_{nm} with the corresponding component of \mathbf{E}_{nm} .

We at first adopt this approach and simulated the NC FET having an internal metal film depicted in Fig. 3(a). The resulting drain current I_D vs gate-to-source voltage V_{GS} characteristic is shown in Fig. 4(a). The figure also shows the I_D - V_{GS} characteristic of the corresponding metal-oxide-semiconductor (MOS) FET depicted in Fig. 3(b). Comparing the two I_D - V_{GS} characteristics, we can see that the I_D of the NC FET increases more steeply. This is due to the gate voltage modulation by the NC of the ferroelectric film, which is shown in Fig. 4(b). The figure shows the difference between the electrostatic potentials at the lower and upper interfaces of the ferroelectric film, ΔV_G , plotted as a function of V_{GS} and indicates that the NC works as expected. In other words, it reduces the voltage applied to the MOS structure underlying the ferroelectric film at a low V_{GS} , while at a high V_{GS} it enhances the voltage. It is note that the cross marks in Fig. 4 show the device state just before the gate capacitance becomes negative and consequently the simulation is terminated.

B. Coupled Solution

In the approach described in the previous subsection, (6) is solved separately from the Poisson and current continuity equations, that is, it is not included in the equation $f(\mathbf{v}) = \mathbf{0}$ mentioned in Section II-A and is solved when the vector $f(\mathbf{v})$ and the Jacobian $\partial f(\mathbf{v}_0)/\partial \mathbf{v}$ are being constructed to solve (3). Fortunately, as long as the electric field is smaller than E_c in the ferroelectric film, (6) can be easily solved by using the Newton method and the assumption that the film is in an NC state. However, a device simulator should have an error handler for the failure of solving (6). Such special

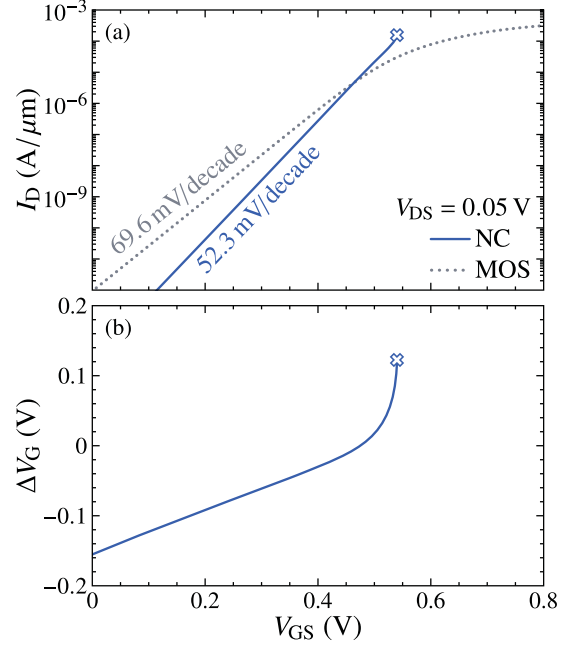


Fig. 4. (a) I_D - V_{GS} characteristics of (solid line) the NC and (dotted line) MOS FETs shown in Figs. 3(a) and 3(b). The simulation was conducted for a drain-to-source voltage V_{DS} of 0.05 V. (b) Voltage across the ferroelectric film or gate voltage modulation ΔV_G of the NC FET plotted as a function of V_{GS} .

treatments of (6) would be a burden to the device simulator in terms of the maintainability and extensibility. For example, when cross terms of the polarization components such as $P_x^2 P_y^2$ are added to the Gibbs free energy density, the three equations of (4) should be solved simultaneously to determine the three components. Also, in a transient simulation, the device simulator should store the polarization distributions in previous time points, although a polarization vector is associated with not a node but an edge and the data structure of its distribution is more complicated than that of the distribution of the electrostatic potential or carrier concentration.

To avoid these issues, there is no alternative but to include (6) in $f(\mathbf{v}) = \mathbf{0}$, which become possible by treating a polarization vector as a property of each nodes similarly to the electrostatic potential and carrier concentration and representing the polarization vector in a control volume by that at its owner node $\mathbf{P}_n = (P_{x,n}, P_{y,n}, P_{z,n})$. Then, P_{nm} in (8) is given by $P_{nm} = P_n \cdot \mathbf{u}_{nm}$ and (6) can be discretized for the n th node as follows:

$$(2\alpha P_{i,n} + 4\beta P_{i,n}^3 + 6\gamma P_{i,n}^5 - E_{i,n})V_n = 0 \quad (i = x, y, z). \quad (9)$$

Here, $E_{i,n}$ is the electric field component along the i -axis at the n th node and is approximated as

$$E_{i,n} = \frac{\sum_m E_{nm} u_{i,nm} S_{nm} |u_{i,nm}|}{\sum_m S_{nm} |u_{i,nm}|} \quad (i = x, y, z) \quad (10)$$

with $u_{i,nm}$ begin the component of \mathbf{u}_{nm} along the i -axis. Equation (9) and \mathbf{P}_n can be included in $f(\mathbf{v}) = \mathbf{0}$ and \mathbf{v} ,

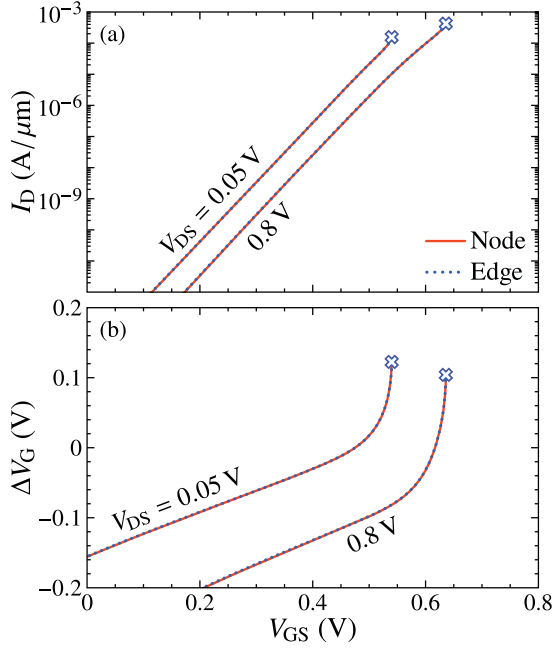


Fig. 5. (a) I_D and (b) ΔV_G in NC FETs calculated as a function of V_{GS} using the methods described in (solid line) Sections III-B and (dotted line) III-A.

respectively, and a device simulator can solve (6) together with the Poisson and current continuity equations. The device simulator is flexible to some extent about this kind of function enhancement because in some cases it is required to simulate the lattice and carrier temperatures in addition to the electrostatic potential and carrier concentration at each node and it may be designed with this requirement in mind.

Fig. 5(a) shows the I_D - V_{GS} characteristics of NC FETs calculated using the approaches mentioned above and in the previous subsection and indicates that the two approaches have a comparable accuracy. This is also confirmed in ΔV_G shown in Fig. 5(b) and the polarization field shown in Figs. 6(a) and 6(b).

C. Domain Structures

The polarization fields in the ferroelectric film shown in Figs. 6(a) and 6(b) vary considerably in the thickness direction near both gate edges and do not look like ferroelectrics because the well-known tendency of polarization vectors to align with each other is not observed. Such tendency can be realized by introducing the penalty for the spatial variation in the polarization into the Gibbs free energy density (5), which for cubic materials is written as [18], [19],

$$g_{dw}(\mathbf{r}) = \frac{\delta_{11}}{2}(P'_{x,x}{}^2 + P'_{y,y}{}^2 + P'_{z,z}{}^2) + \delta_{12}(P'_{x,x}P'_{y,y} + P'_{y,y}P'_{z,z} + P'_{z,z}P'_{x,x}) + \frac{\delta_{44}}{2}[(P'_{x,y} + P'_{y,x})^2 + (P'_{y,z} + P'_{z,y})^2 + (P'_{z,x} + P'_{x,z})^2]. \quad (11)$$

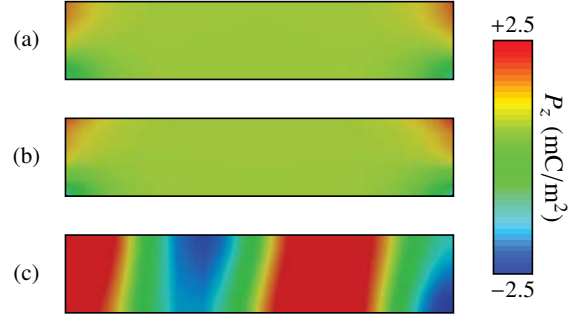


Fig. 6. Distribution of the z -component of the polarization in the ferroelectric film of NC FETs calculated using the methods described in (a) Sections III-A, (b) III-B, and (c) III-C at V_{GS} of 0.4 V and V_{DS} of 0.05 V. In (c), δ was set so that the domain wall thickness T_{dw} is 10 nm.

Here, $P'_{i,j}$ represent $\partial P_i / \partial x_j$ ($x_1 = x$, $x_2 = y$, and $x_3 = z$) and δ_{11} , δ_{12} , and δ_{44} are the material parameters related to the thickness of domain walls. Taking this term into account and assuming that $\delta_{11} = -\delta_{12} = \delta_{44} = \delta$, we can rewrite (6) as

$$2\alpha P_i + 4\beta P_i^3 + 6\gamma P_i^5 - E_i - \delta \nabla \cdot (\nabla P_i) = 0 \quad (i = x, y, z). \quad (12)$$

Fortunately, since this equation has the same form as the Poisson and current continuity equations, its discretized form would be derived without difficulty and we adopted the following form,

$$\sum_m \delta \frac{P_{i,n} - P_{i,m}}{\ell_{nm}} S_{nm} + (2\alpha P_{i,n} + 4\beta P_{i,n}^3 + 6\gamma P_{i,n}^5 - E_{i,n}) V_n = 0 \quad (i = x, y, z). \quad (13)$$

The I_D and ΔV_G in NC FETs having a ferroelectric film with non-zero or zero δ are shown in Figs. 7(a) and 7(b), respectively. In the simulation, the non-zero δ value was chosen so that the domain wall thickness T_{dw} is 10 nm, which is given by [19]

$$T_{dw} = P_r \sqrt{\frac{2\delta}{\beta P_r^4 + 2\gamma P_r^6}}. \quad (14)$$

Here, the remanent polarization P_r satisfies

$$P_r^2 = \frac{-\beta + \sqrt{\beta^2 - 3\alpha\gamma}}{3\gamma}. \quad (15)$$

Figs. 6(c) and 6(b) show the polarization fields in the ferroelectric films with non-zero and zero δ 's, respectively. In the case of zero δ , a variation in the polarization is concentrated at the film edges. On the other hand, in the case of non-zero δ , such a variation spreads throughout the film due to $g_{dw}(\mathbf{r})$ and a domain structure appears. We have confirmed that the domain size increases with increasing δ . Although the introduction of $g_{dw}(\mathbf{r})$ drastically changes the polarization field, such a noticeable change is not observed in the behaviors of NC FETs and ferroelectric films, as shown in Fig. 7. Even when a domain structure appears, upward and downward polarizations do not completely cancel each other out because

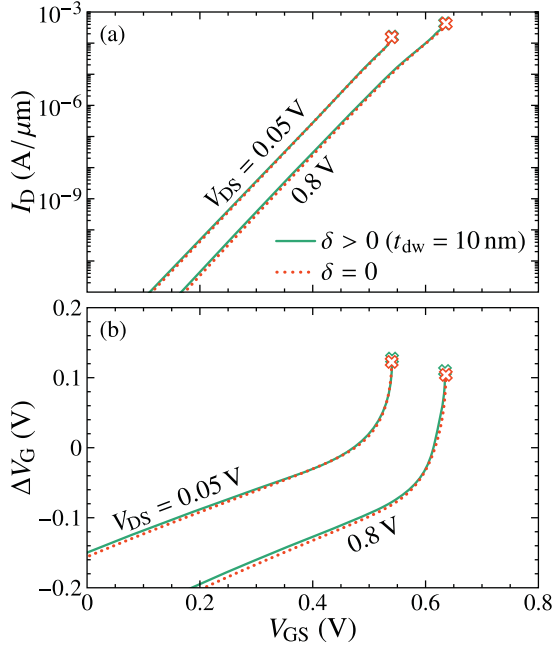


Fig. 7. (a) I_D and (b) ΔV_G in NC FETs having ferroelectric films with (solid line) non-zero and zero δ 's. The non-zero δ was set so that $T_{dw} = 10$ nm.

of the differences in the magnitude and domain size, which reproduce the function of NC.

Since NC states are likely to stably appear in single domain structures [20], the internal metal film is expected to uniformly apply an electric field to the ferroelectric film, thereby aligning the directions of the polarization and facilitating the appearance of the NC states. However, the appearance of the multi domain structure shown in Fig. 6(c) implies that the internal metal film may not play the expected role in NC FETs. We have confirmed that the behavior of NC FETs without the internal metal film can be simulated relatively stably if δ is large to some extent. Fig. 8 shows the I_D - V_{GS} characteristics of NC FETs without and with the internal metal films and indicates that the NC FETs lost the steep switching feature together with the film. We will discuss this interesting result elsewhere.

IV. CONCLUSIONS

In this work, we have studied the method to simulate NC FETs having a gate insulator film made of a ferroelectric material in the framework of TCAD device simulation. We adopt the Landau-Khalatnikov equation to describe the behavior of the polarization in ferroelectrics and proposed a method to solve this equation simultaneously with standard equations describing electronic devices' behavior. The method has been incorporated into our own device simulator and it has been confirmed by the simulator that the method can simulate NC FETs with an accuracy comparable to another method which solves the Landau-Khalatnikov equation separately from the other equations. Owing to its applicability to various

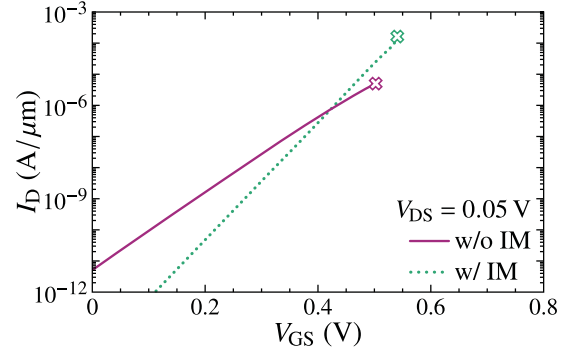


Fig. 8. I_D - V_{GS} characteristics of NC FETs without the internal metal film, which is merged into the gate oxide film while maintaining its capacitance. The results obtained for NC FETs with the internal metal film are also shown by the dotted line. The simulation was carried out for T_{dw} of 10 nm.

ferroelectric models, we successfully introduced a factor describing the tendency of polarization vectors to align with each other in ferroelectrics into the simulation and consequently observed that domain structures of the polarization appear in the ferroelectric film of NC FETs even when the film is sandwiched between the internal metal film and gate electrode. The proposed method will be helpful to improve the device simulation and then promote the research and development of NC FETs.

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