A Hybrid Spin-charge Mixed-mode Simulation Framework for Evaluating STT-MRAM Bit-cells Utilizing Multiferroic Tunnel Junctions

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Abstract—Multiferroic tunnel junctions (MFTJs) consisting of ferromagnetic contacts sandwiching a ferroelectric tunnel barrier have been proposed as possible data storage elements. However, a simulation framework is needed for evaluating and analyzing the design and performance of memory cells based on MFTJs. In this paper, we propose a spin-charge mixed-mode simulation framework that captures the device physics of the MFTJ for SPICE circuit simulations.

I. INTRODUCTION

The emergence of spin-transfer torque MRAM (STT-MRAM) as a strong candidate for future universal memory technology (due to its non-volatility, compatibility with CMOS technology, scalability and reasonably high performance [1]) has led to significant research interest. However, STT-MRAM bit-cells suffer from conflicting design requirements for reading from and writing to the cell [2]. The tunneling magnetoresistance is used for sensing data stored in STT-MRAM bit-cells. The MTJ consists of a pinned and a free magnetic layer (PL and FL, respectively) sandwiching a tunnel barrier. MTJ conductance depends on the relative magnetization of PL and FL, and is switched between high and low conductance in STT-MRAM by manipulating FL magnetization. Analysis of 1T-1MTJ STT-MRAM bit-cells show that sensing failures may severely limit the bit-cell area and failure probability [3]. Enhancing the tunneling magnetoresistance effect may improve the sensing failure probability of STT-MRAM bit-cells. Replacing the tunnel barrier in an MTJ with a ferroelectric tunnel barrier (FTB) allows modulation of the tunneling conductance through the tunneling electroresistance (TER) effect [4], which may be used to enhance the tunneling magnetoresistance ratio (TMR) of the tunnel junction (TJ) and improve sensing



Fig. 1. Flow chart of our proposed simulation framework.

failures in STT-MRAM memory cells. However, a simulation framework that captures both tunneling electroresistance and tunneling magnetoresistance effects is needed to evaluate the effectiveness of magnetic ferroelectric tunnel junctions (MFTJs) on improving STT-MRAM sensing failure. This paper presents a mixed-mode simulation framework that captures both effects in the device for circuit level simulations. The rest of this paper is organized as follows. Section II describes our proposed simulation framework. The MFTJ structure analyzed using our proposed simulation framework is then presented in Section III. Finally, Section IV discusses the simulation results and Section V concludes this paper.

II. PROPOSED SIMULATION FRAMEWORK

Our proposed simulation framework (Fig. 1) consists of 1) a Landau-Lifshitz-Gilbert (LLG) model for capturing magnetization dynamics, 2) a Landau-Khalatnikov (LK) dynamical ferroelectric model, 3) a Non-Equilibrium Green's Function (NEGF) formalism based solver for electron transport, and 4) a SPICE compatible circuit model for MFTJ.

A. Landau-Lifshitz-Gilbert (LLG) Model

Magnetization dynamics of the FL affect the MFTJ resistance (R_{MFTJ}) and needs to be modeled in order to properly describe MFTJ dynamics during switching. In scaled STT-MRAM technology, the FL can be approximated as a monodomain magnet. Magnetization dynamics of a mono-domain magnet is obtained by solving the Landau-Lifshitz-Gilbert (LLG) equation shown as (1). γ is the gyromagnetic ratio, α is the Gilbert damping factor, \hat{m} is the magnetization vector of the nano-magnet, \vec{H}_{EFF} is the effective magnetic field on the nano-magnet and STT is the spin torque term which will be discussed in Section II-C.

 \dot{H}_{EFF} captures all magnetic-field like interactions as shown in (2). \vec{H}_{UNI} is the uniaxial anisotropy field, \vec{H}_{EXT} is the externally applied magnetic field, \vec{H}_{DEMAG} is the demagnetization field, \vec{H}_{DIP} is the dipolar field from other nano-magnets, and \vec{H}_{TH} is the thermal fluctuation field. Using Cartesian coordinates, the equilibrium direction (or the easy axis) of the magnet is defined to be along the z-axis (\hat{z}). Thus, \hat{m} is either $+\hat{z}$ or $-\hat{z}$ in equilibrium. The activation energy (E_A) is used to calculate the uniaxial anisotropy field using (3)-(4), where M_S and V are the saturation magnetization and



Fig. 2. Conceptual description of the MFTJ in the NEGF framework, where each cross represents a lattice point. The potential profile across the MFTJ under different FE polarizations without spin splitting are also shown.

volume of FL, respectively (and $E_A = Ku_2V$). Anisotropies of nano-magnets with in-plane anisotropy (IMA) may be modeled using (3), whereas anisotropies of nano-magnets with perpendicular magnetic anisotropy (PMA) may be modeled using (4) [5]. In an STT-MRAM array where there are many nano-magnets, the dipolar coupling between nano-magnets may be captured using (5), where for the *i*-th nano-magnet, $\vec{r_i}$ is the vector from the nano-magnet to FL, $\hat{m_i}$ describes its magnetization direction, and μ_r is the relative permeability of the material in which all nano-magnets and FL are buried in. Temperature effects as prescribed by Brown are captured in (6), where the components of $\vec{\xi}$ are independent Gaussian random variables. k_B is the Boltzmann constant and T is the absolute temperature.

B. Landau-Khalatnikov (LK) Model

Dynamics of ferroelectric polarization is described by the Landau-Khalatnikov equation [6] as in (7), where $F(\vec{P})$ is the free energy functional of the ferroelectric material, and a_0 is a proportionality constant. In our model, $F(\vec{P})$ is written as (8), where $F_0(\vec{P})$ describes the ferroelectric anisotropy, a_1 is a proportionality constant, and \vec{E} is the external electric field applied across the ferroelectric.

C. Non-Equilibrium Green's Function (NEGF) Method

The Non-Equilibrium Green's Function (NEGF) formalism [7] is used to calculate the I - V characteristic of the MFTJ, which is used in the SPICE circuit model of the MFTJ for circuit simulations. The NEGF formalism may also be used to calculate the spin torque acting on the free layer (FL) of the MFTJ when solving for the magnetization dynamics of FL.



Fig. 3. Block diagram of our SPICE compatible MFTJ model.

We first write the MFTJ Hamiltonian, \mathcal{H} , as (9), where \mathcal{H}_0 and \mathcal{H}_S are spin-independent and spin-dependent parts of \mathcal{H} , respectively. \mathcal{H}_0 may be written as in (14)-(16), where I is the 2×2 identity matrix, and N_L and N_R are the indices of the left and right interface lattice point, respectively (Fig. 2). \hbar is the reduced Planck's constant, m_l^{*} is the effective electron mass in region l, and a is the uniform lattice spacing used for numerical calculation. The spin-dependent part is written as in (17), where $\vec{\sigma}$ represents the Pauli spin matrices, and \hat{m} and Δ are the magnetization direction and the conduction band splitting of up-spins and down-spins at the lattice point, respectively. The barrier height of the tunnel barrier (U_B) is included in U_{OX} as in (18), while the voltage applied across the MFTJ (V_{MFTJ}) is modeled using U_{APP} as in (19). The effect of ferroelectric polarization in the tunneling barrier is modeled using (20)-(22), where δ_l and ϵ_l are the Thomas-Fermi screening length and relative permittivity of electrode *l*, respectively. Finally, the current density flowing through the MFTJ (J_{MFTJ}) can be calculated using (25). However, J_{MFTJ} depends on the relative angle between PL and FL (θ) , and on the polarization of the ferroelectric tunnel barrier (P). In our model, the dependence of J_{MFTJ} on θ and on P are decoupled. Hence, for a fixed P, $J_{MFTJ}(\theta)$ may be calculated using (26), where $J_P = J_{MFTJ}(\theta = 0)$ and $J_{AP} = J_{MFTJ}(\theta = \pi)$. $J_{MFTJ}(P)$ may then be written as (27), where c_i are fitting parameters (different for +P and for P) since P modulates the effective barrier height [4].

D. SPICE Compatible Dynamical MFTJ Model

The components of our SPICE compatible dynamical MFTJ model are shown in Fig. 3. The I - V characteristics of the MFTJ returned by our NEGF solver are encapsulated as a compact model, and may also include \overrightarrow{STT} calculated using (28)-(29) in the NEGF solver. Alternatively, the model for STT proposed in [8], written as (30)-(34), may also be used. Our SPICE model includes two blocks that allows SPICE to solve the LLG and LK equations during simulation of MFTJ based STT-MRAM memory cells. Each block consists of a capacitor network as shown in Fig. 3, where each current source represents one term in the differential equation and capacitor voltages are P and components of \hat{m} , in spherical coordinates in the LK and LLG block, respectively, P and \hat{m} are used to calculate I_{MFTJ} , V_{MFTJ} , and \overrightarrow{STT} during simulation.

$$LLG: \frac{\partial \hat{m}}{\partial t} = -|\gamma| \hat{m} \times \vec{H}_{EFF} + \alpha \hat{m} \times \frac{\partial \hat{m}}{\partial t} + \vec{STT} \quad (1) \quad \vec{H}_{EFF} = \vec{H}_{UNI} + \vec{H}_{EXT} + \vec{H}_{DEMAG} + \vec{H}_{DIP} + \vec{H}_{TH} \quad (2)$$

$$\vec{H}_{DIP,j} = \sum_{i} \mu_{r} \frac{3 \left(\frac{M_{S,i}}{M_{S,j}} \hat{m}_{i} \cdot \vec{r}_{i}\right)}{|\vec{r}_{i}^{*}|^{6}} \vec{r}_{i}^{*} \quad (3) \quad \text{In-plane anisotropy:} \quad \vec{H}_{UNI} + \vec{H}_{DEMAG} = \frac{2Ku_{2}}{M_{S}} \hat{m}_{Z} - 4\pi M_{S} \hat{m}_{Y} \quad (5)$$

$$\vec{H}_{TH} = \vec{\xi} \sqrt{\frac{2\alpha k_{B}T}{|\gamma|M_{S}V}} \quad (4) \quad \text{Perpendicular anisotropy:} \quad \vec{H}_{UNI} + \vec{H}_{DEMAG} = \left(\frac{2Ku_{2}}{M_{S}} - 4\pi M_{S}\right) \hat{m}_{Z} \quad (6)$$

$$LK: \quad \frac{\partial \vec{F}}{\partial t} = -a_{0} \frac{\partial F(\vec{F})}{\partial \vec{F}} \quad (7) \quad F\left(\vec{F}\right) = F_{0}\left(\vec{F}\right) + a_{1}\vec{E} \cdot \vec{F} \quad (8)$$

$$\begin{split} \frac{1}{|\nabla F|^{2}} & = -\frac{1}{|\partial F|^{2}} (Y)^{2} + \left(\frac{1}{|\nabla F|^{2}}\right)^{2} (Y)^{2} (Y)^{2}$$



Fig. 4. Successful calibration of our NEGF solver to experimental data reported (left) in [9] and (right) in [5].

E. Model Calibration

Our models are first calibrated to experimental data published in the literature before simulation. Since no experimental data on MFTJ is available (to the authors' best knowledge), our models were calibrated to MTJ measurements in [5], [9], [10] instead. Calibration results of our NEGF solver as shown in Fig. 4 shows our model and experimental data are in good agreement. Our LLG solver was calibrated to [10] as shown in Fig. 5. Results showed switching times between 4.5ns and 5.5ns, and are in good agreement with experimental data published in [10].

III. THE MULTIFERROIC TUNNEL JUNCTION (MFTJ)

The MFTJ structure (Fig. 6) consists of two ferromagnetic electrodes sandwiching an FTB. Ferromagnetic configuration of the MFTJ is switched using spin-transfer torque like



Fig. 5. Successful calibration of our SPICE model (without ferroelectric polarization) to experimental data reported in [10]. Worst case switching times for (a) Antiparallel to parallel and (b) parallel to antiparallel switching were 4.5ns and 5.5ns, respectively. The definition of (c) standard and (d) reversed cell connections are also shown.

in MTJ-based STT-MRAM. The current directions for antiparallelizing (I_{AP}) and for parallelizing (I_P) the FL are shown in Fig. 6. Since the FTB is very thin, the electric field in the tunnel barrier during write operations may be sufficient to switch the FTB polarization when current is being passed through MFTJ to switch its FL magnetization. Hence, two configurations of ferroic properties exist in the structure as shown in Fig. 6. The remnant polarization in the FTB and nonzero screening lengths in the ferromagnetic electrodes result in a small TER effect as illustrated by the band diagrams in Fig. 2. The effective potential along the transport direction of the MFTJ is such that the barrier height is larger when FTB polarization points toward the electrode with the larger screening length. Although the TER effect is small when FTB is thin, it may be sufficient to enhance the TMR of the MFTJ and hence, reduce sensing failures in STT-MRAM.



Fig. 6. The MFTJ structure consists of two ferromagnetic (FM) layers (blue with red arrows) sandwiching a thin ferroelectric layer (gray with dark blue arrows). The arrows denote the magnetization and electric polarization of the ferromagnetic and ferroelectric layers, respectively. In-plane anisotropy (IMA) FM layers are shown for illustration. The two memory states available are shown. (Right) The circuit schematic of the MFTJ based STT-MRAM memory cell with PL on the bottom. I_{AP} and I_P denote the current directions for anti-parallelizing and parallelizing the FM layers, respectively.

IV. SIMULATION RESULTS

We investigated an MFTJ by adding ferroelectic polarization to the MTJ from our calibration steps. The ferroelectric polarization versus electric field hysteresis curve and the ferromagnetic parameters assumed for the MFTJ are shown in Fig. 7 (t_{OX} in the MFTJ case is the equivalent MTJ t_{OX}). \vec{P} is assumed to be pointing along the direction of electron transport. Device level TMR versus oxide voltage were calculated in our NEGF solver and plotted in Fig. 8, showing that the *TMR* of the MFTJ is 7.2% higher than that of the MTJ. However, the TMR of the MFTJ based STT-MRAM memory cell is only 4.7% higher than MTJ based STT-MRAM (assuming 900nm wide access transistor), implying that transistor resistance significantly affects TMR of the memory cell. Although the FTB enhanced the TMR of MFTJ based STT-MRAM, the overall resistance of the memory cell is also higher than that of MTJ based STT-MRAM. Consequently, read disturb current through the MFTJ based STT-MRAM is $0.3\mu A$ lower than in MTJ based STT-MRAM. Read disturb failures are thus lower in MFTJ based STT-MRAM than in MTJ based STT-MRAM. On the other hand, due to the larger resistance, MFTJ based STT-MRAM requires a write voltage of 0.973V compared to 0.971V in MTJ based STT-MRAM (considering 10% write margin, where write margin = $\frac{I_{WRITE} - I_{C0}}{I_{C0}} \times 100\%$).

V. CONCLUSION

We presented a hybrid spin-charge mixed-mode simulation framework for MFTJ based STT-MRAM. Our proposed simulation framework allows self-consistent solution of NEGF, LLG, and LK equations for SPICE simulation of STT-MRAM memory cells. Simulation results show that MFTJ based STT-MRAM memory cells can have slightly higher ($\sim 5\%$ higher *TMR* and lower read disturb failures as compared to MTJ based STT-MRAM memory cells. However, MFTJ based STT-MRAM memory cells require a higher write voltage as MFTJ resistance is higher compared to MTJ based STT-MRAM.

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Fig. 7. (Left) Ferroelectric polarization vs. applied voltage curve of MFTJ and (right) other simulation parameters (other than those in Fig. 4 used to calibrate our model to data in [5]) assumed in this work.



Fig. 8. Comparison of device TMR of MFTJ and MTJ.

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