# Numerical Performance Analysis of Carbon Nanotube (CNT) Embedded MOSFETs

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#### Abstract

We analyze a novel MOSFET design that has semiconducting single-walled zig-zag Carbon Nanotubes (CNTs) in the channel. We also report on a modeling technique for simulating CNT-MOSFET devices. Our investigations have shown that CNT-MOSFETs employing small diameter CNTs (d=8Å) show low leakage and negative differential conductance. Midrange CNT-MOSFETs (d=13Å) show improved current drive. However larger diameter CNTs (d=17Å) result in degraded MOSFET performance due to effective punch-through and bandoffsets.

# **1** Device Model for CNT-MOSFETs

Recent experiments [1] and theoretical studies [2] indicate that semiconducting CNTs have the highest known mobility among semiconductors. Therefore CNTs have prompted large interest in research [1]-[5]. We here discuss models for numerical simulation of CNT embedded MOSFETs for feasibility analysis.



Figure 1: Proposed CNT-MOSFET.

We solve the Poisson equation along with modified electron-hole current continuity equations to obtain current-voltage characteristics of CNT-MOSFETs shown in Fig. 1. Our modified semiconductor equations account for quantum effects and band discontinuities. The complete set of equations is shown below:

$$\nabla^2 \phi = -\frac{q}{\epsilon} (p - n + D) \tag{1}$$

$$\frac{\partial n}{\partial t} = \frac{1}{q} \nabla J_n - R_n + G_n \tag{2}$$

$$\frac{\partial p}{\partial t} = -\frac{1}{q} \nabla J_p - R_p + G_p \tag{3}$$

The electron current density  $J_n$  and hole current density  $J_p$  are defined as follows:

$$J_n = -qn\mu_n \nabla(\phi + \phi_{QM} + \phi_e) + q\mu_n V_{th} \nabla n \tag{4}$$

$$J_p = -qp\mu_p \nabla(\phi - \phi_{QM} - \phi_h) - q\mu_p V_{th} \nabla p \tag{5}$$

Here in addition to  $\phi$ , we have effective potentials  $\phi_{QM}$ ,  $\phi_e$  and  $\phi_h$  to account for quantum and heterostructure effects.

# **1.1 Quantum Effective Potential**

We use density gradient formalism to account for quantum confinement effects at the channel of MOSFET and at the band discontinuities between the CNT and silicon (Si). In this formalism quantum correction is defined as follows [6]:

$$\phi_{QM} = \frac{2\hbar^2}{12q\sqrt{n}} \left(\frac{1}{m_{\parallel}} \frac{\partial^2 \sqrt{n}}{\partial x^2} + \frac{1}{m_{\perp}} \frac{\partial^2 \sqrt{n}}{\partial y^2}\right) \tag{6}$$

Here x and y are parallel and perpendicular directions to the MOSFET channel. We set the effective mass to that of Si or CNT depending on the location.

# **1.2 CNT-Si Effective Potential**

We also calculate the effects of conduction and valence band offsets on electrons and holes through  $\phi_e$  and  $\phi_h$ , respectively [7]. They account for the variations in bandgaps and electron affinities between Si and the CNT, and are expressed as:

$$\phi_e = \frac{1}{q} \left( \chi^{CNT} - \chi^{Si} \right) + V_{th} ln \left( \frac{n_o^{CNT}}{n_o^{Si}} \right) \tag{7}$$

$$= \frac{1}{q} \Delta E_C + V_{th} ln(\frac{n_o^{S_t}}{n_o^{S_t}}) \tag{8}$$

$$\phi_h = -\frac{1}{q} \left( \chi^{CNT} - \chi^{Si} + E_G^{CNT} - E_G^{Si} \right) - V_{th} ln(\frac{n_o^{CNT}}{n_o^{Si}})$$
(9)

$$= -\frac{1}{q}\Delta E_V - V_{th} ln(\frac{n_o^{CNT}}{n_o^{ST}})$$
(10)

Here  $\chi$ ,  $n_o$ ,  $E_G$  symbolize electron affinity, intrinsic carrier concentration and bandgap of Si and CNT, respectively.  $\Delta E_C$  and  $\Delta E_V$  are conduction and valence band discontinuities between Si and CNT. These effective potentials account for the contact resistance between Si and CNT which arises due to mismatch in the bandstructure.

# **1.3 Band Parameters of CNTs**

To determine the electron affinity and bandgap of CNTs, we use the band diagram and electron affinity of graphite [2].

Our analyses indicate that bandgaps of semiconducting single-walled zig-zag CNTs are inversely proportional to their diameter. We here employ three different CNTs that have diameters (d) of 8Å, 13Å and 17Å. These CNTs have the respective calculated bandgap values of 1.05eV, 0.67eV and 0.48eV. We later use these bandgap values to calculate CNT electron affinities. Corresponding CNT affinities are then the electron affinity of graphite (4.4eV [8]) minus half the bandgap value. We next calculate the last term in the effective potential due to bandgap variations. Using nondegenerate statistics and the zero energy point at the midgap we obtained the following expression for the intrinsic carrier concentration of CNTs:

$$n_o^{CNT} = \sum_l \frac{1}{d^2} \sqrt{\frac{q V_{th} m^{*l}}{2\pi \hbar^2}} e^{-\frac{E^l}{q V_{th}}}$$
(11)

Here  $m^{*l}$  and  $E^l$  are CNT effective mass and energy in subband l, respectively.

# **1.4 Mobility Model for CNTs**

We last need a mobility model to be used in the semiconductor equations. We employed a Monte Carlo (MC) simulator to obtain electron drift velocity versus electric field curves [2]. The simulations show high scattering rates for narrow CNTs, which allows for the extraction of mobility models. Our calculated velocity versus field curves show that CNTs attain very large drift velocities and show negative differential mobility (NDM). They are similar to GaAs in that respect, where conduction velocity of the first subband is larger than that of the second. We then developed an analytical model considering the lowest two subbands that give rise to the NDM effect. We next express the final mobility as a function of electric field and tube diameter using Mathiessen's rule:

$$\frac{1}{\mu(F,d)} = \frac{1}{\mu_1(F,d)} + \frac{1}{\mu_2(F,d)}$$
(12)

$$\mu_1(F,d) = \frac{\mu_o(d)}{1 + \frac{F}{F_c(d)}}$$
(13)

$$\mu_2(F,d) = \frac{V_m(d)}{F(1+\lambda \frac{F}{F_c(d)})}$$
(14)

Here  $\mu_o(d)$  is the low field CNT mobility and  $F_c(d)$  is the electric field where CNT reaches peak drift velocity  $V_m(d)$ . Mobility of the second subband additionally includes an empirical parameter  $\lambda$ .

#### 2 Simulation Results

We used the doping profile of a well-tempered MOSFET with channel length of 150nm. We then embed a planar sheet of CNTs at the topmost part of the channel of this MOS-FET. We simulated three different CNTs with diameters of 8 Å, 13 Å and 17 Å.

Figure 2 shows the current-voltage characteristics for  $V_{GS}$ = 1.0V. The CNT-MOSFET with the medium diameter tube has improved drive current capabilities largely due to the very high mobility in CNTs. However the biggest tubed CNT-MOSFET has degraded operation, with behavior that is more like a resistor. This can be attributed to the inability of MOSFETs with larger CNTs to form a pinchoff region. This may be related to the high intrinsic carrier concentration (roughly 10<sup>16</sup> cm<sup>-3</sup>) that is found in the largest tube. In addition, lowest diameter tubed device also operates like a MOSFET with drive currents slightly less than the conventional MOSFET under the specified conditions. The narrowest tubed device also shows inflection point towards high drain bias that can be related to the Schottky barrier between Si and CNT.



Figure 2: CNT-MOSFET current-voltage characteristics for  $V_{GS}$ =1.0V and different diameter CNTs.

Figure 3: CNT-MOSFET drain currents for  $V_{DS}$ =1.2V. Inset is the linear scale plot around  $V_{GS}$ =1.36V.

We find that all CNT-MOSFETs show good subthreshold characteristics under low drain bias. However CNT-MOSFETs with larger diameter tubes start exhibiting leakier performance under high drain bias. Figure 3 shows the current-voltage characteristics for  $V_{DS}$ = 1.2V. For high drain bias the largest diameter CNT-MOSFET is very leaky. The MOSFET with the medium size tube also suffers drain induced barrier lowering which we associate it with the effects of the junction barrier between Si and CNT. The lowest diameter CNT-MOSFET performs best among all. In addition, the small diameter tube shows negative differential conductance that might be attributed to the barrier lowering at the Si-CNT junction and the effects of NDM.

In conclusion, simulations indicate that the CNT-MOSFET with small and medium diameter tubes show potentially improved performance over the conventional MOSFET. However performance degradation quickly sets in as the CNT diameter increases. In addition, CNT devices that use the NDM attribute of CNT mobility have the potential to be engineered for use in oscillators or related devices.

## References

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