

# Investigation of Silicon Carbide Diode Structures via Numerical Simulations Including Anisotropic Effects

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## Abstract

The Poisson's and continuity equations based SiC-DYNAMIT-1DT/2DT simulators with anisotropic material analysis capabilities were developed. A comparison of experimental and simulated forward I/V curves for three 6H-SiC P<sup>+</sup>NN<sup>+</sup> diodes is presented and the related model parameter adjustment problems are discussed. The influence of the strong electron mobility anisotropy on carrier distributions is investigated and the existence of a "mobility anisotropy induced anomalous charge accumulation effect" is demonstrated.

## 1. Introduction

The progress in SiC device technology is enhancing the development of the simulators in order to link material data to the electrical behaviour of devices. Recently, a 4.5kV 6H-SiC diode has been reported [1] with acceptable electrical performance. However, in this work, like in others, e.g. [2,3] the measured results, particularly the high forward current voltage drops, disagree with simulated results. This emphasises the need of a correct physical description, i.e. first of all the development of temperature and doping-dependent lifetime and mobility models. Another problem for simulator development is the material anisotropy, especially related to 6H-SiC (power applications), and less to 4H-SiC (high-frequency applications). In the following, a remarkable influence of the strong electron mobility anisotropy on the forward-biased pn-junction behaviour is demonstrated.

In connection with the present work, the 1D and 2D nonisothermal simulators SiC-DYNAMIT-1DT/2DT, developed at Tallinn Technical University, were also tested against the recent version of MEDICI [4] with anisotropic material simulation capabilities (but without any physical default data). If the same input data and mesh were carefully specified for forward-biased tasks like the ones described below, then a fair coincidence of results was obtained.

## 2. Comparison of experimental and simulated forward I/V characteristics

In the present 1D-simulations the  $n_i(T)$  [5],  $\mu_{n||}, \mu_p(N_D+N_A, T)$  [6],  $\epsilon_{||}=10.03$  [7] ( $\parallel$  denotes "parallel to c-axis") and the conventional single-level SRH recombination model were used. The doping-dependent bandgap narrowing, Auger recombination, electron-hole scattering, mobility field-dependence and incomplete impurity ionization models were omitted, mainly due to the lack of reliable data. The impact ionization [5] was not applied either in the present forward-biased simulations. Fig.1 shows results for a realistic 6H-SiC diode structure [3].

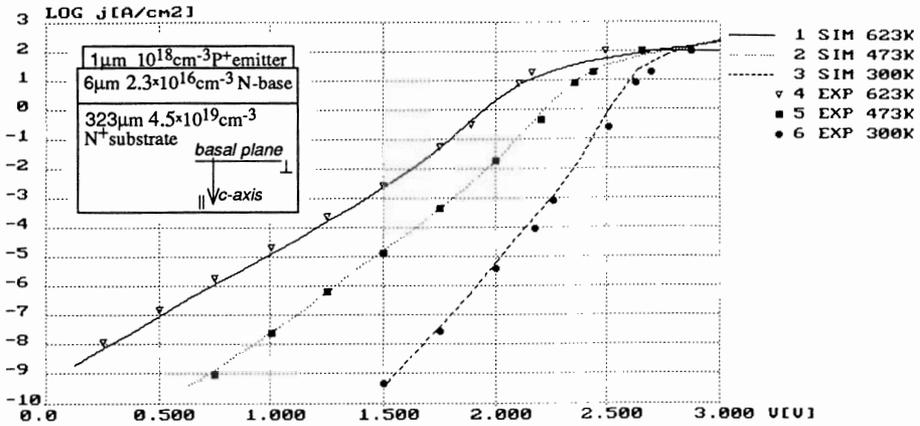


Fig.1 Simulated and experimental forward I/V characteristics at 3 temperatures

To achieve a better fit in Fig.1, the mobility temperature-dependence exponent value of -1.8 was added to the Caughey-Thomas like doping-dependence formulae from a recent work of Cree Corp. researchers [6] (authors of [6] suggest values -2.07...-1.8). Greater exponent absolute values would too strongly decrease mobilities at higher temperatures, and it would cause the I/V curves crossing point to move down to current densities not matching present experiments. For SRH capture times the values  $\tau_{po}(300K)=\tau_{no}(300K)=0.2ns$  (emitters) and 2ns (base) were used together with the temperature-dependence exponent of 1.5 (applied only for the base region). However, it should be pointed out, that to achieve better fit, considerably lower lifetime values have been used here in comparison with the 100ns range lifetimes observed in [1].

Fig.2 gives comparison results for two analogous diode structures, for which the matching of simulation and experiment is quite poor. These 4.5kV and 2.2kV structures were specified according to refs. [1] and [2], respectively. In the simulations  $\tau_{po}=\tau_{no}=1ns$  was taken for all regions, except N-bases where  $\tau_{po}=430ns$  [1] (4.5kV device) and  $\tau_{po}=100ns$  (2.2kV device) were set.

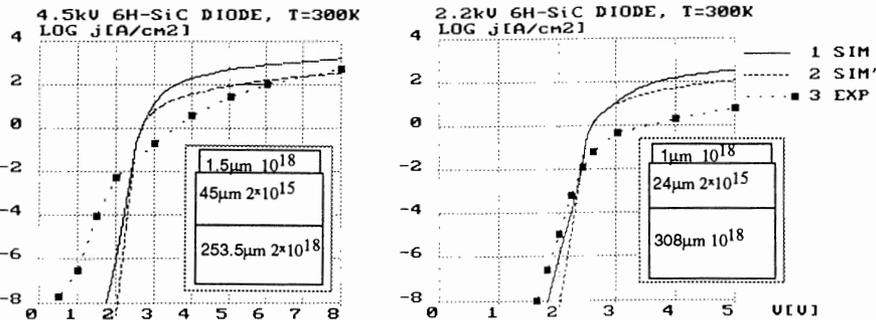


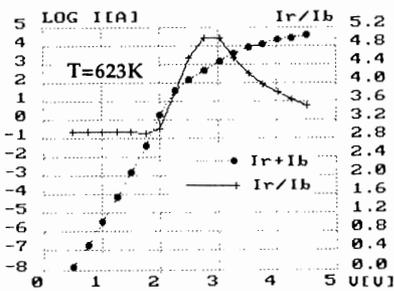
Fig.2 Some additional comparison results for two high-voltage diode structures

Main discrepancies between simulation and experiment can be observed at higher current densities, where simulated voltage drops remain remarkably lower than experimental ones. This may be due to additional contact or probe resistances in experiments, but, for instance, can be explained also as a result of declining lifetime injection-dependence caused by non-midgap recombination centers. The dashed lines in Fig.2 correspond to simulations, where the SRH recombination model parameters  $n_1, p_1$  were specially chosen to achieve a 10-fold lifetime decrease at high injection. Mismatch in the low-current region of the 4.5kV device may be caused by device self-heating during the experiments.

### 3. Two-dimensional simulations with anisotropic effects

To investigate the anisotropy influence, a special  $16\mu\text{m}\times 16\mu\text{m}$  2D-structure was constructed (see Fig.4). The doping profile of this abrupt-junction diode has been taken by the diode from Fig.1 (but including only  $1\mu\text{m}$  of the thick  $\text{N}^+$ substrate). Furthermore, it has a total geometrical symmetry with respect to the  $x=y$  line. The right and bottom contacts were grounded and the voltage was applied to the  $\text{P}^+$ emitter contact in the upper left part of structure. The dimensions were selected so that 1A current corresponds to an average current density  $\approx 1\text{A}/\text{cm}^2$  for all electrodes. Thus any anisotropy influence could clearly be observed as a difference between the right and bottom contact currents or as a symmetry distortion in physical quantity distributions.

While not paying attention to breakdown operation modes (strong impact ionization anisotropy) and nonisothermal heat transfer problems (heat conductivity anisotropy  $\approx 0.7$  for  $6\text{H-SiC}$ ), it can be stated for  $6\text{H-SiC}$ , that beside the low anisotropy of the dielectric permittivity  $\epsilon_{\perp}/\epsilon_{\parallel} = 9.66/10.03$  [7], the  $\mu_n$  anisotropy  $\mu_{n\perp}/\mu_{n\parallel} = 4.8$  is already a remarkable number and it stays quite constant over a wide temperature and doping range [6]. Reverse-biased simulations show that the weak 4%  $\epsilon$  anisotropy causes only  $\approx 2\%$  difference in the field penetration depth and maximum field, following a square root law.



Figs.3,4 present the main results of the forward-biased simulations to investigate the mobility anisotropy influence. The ratio of horizontal (i.e. direction with greater mobility) to vertical current moves from a constant level at low currents to the peak at medium currents. At high currents, the transition to a more uniform current distribution may be observed again (see also the flowlines in Fig.4). As numerical experiments show, these changes are defined both by mobility anisotropy and structure geometry.

Fig.3 Right and bottom contact current sum and ratio versus forward bias

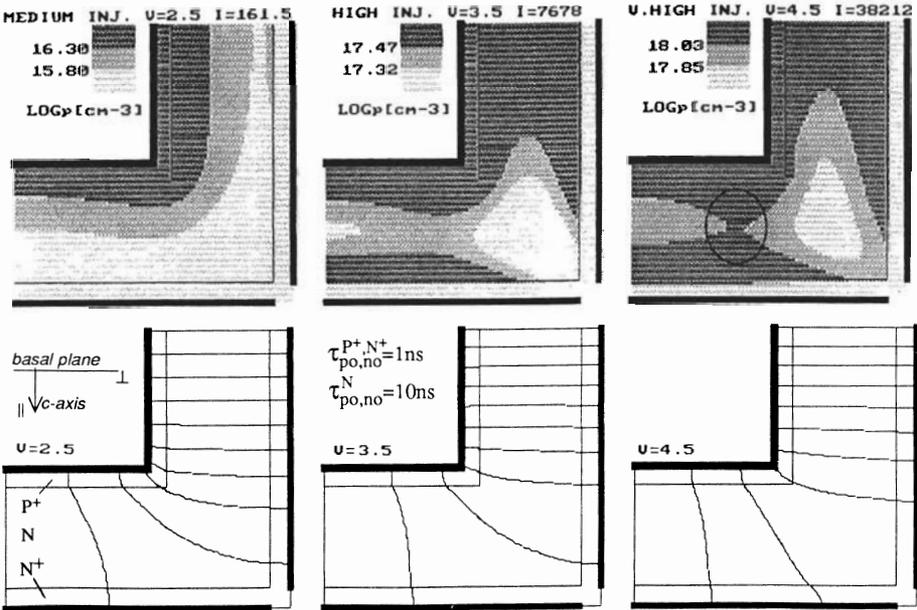


Fig.4 Hole density and total current flowline maps at three forward bias

### 4. The anomalous charge accumulation effect

Fig.4 shows that the "normal" p(x,y) distributions (where higher carrier density corresponds to higher mobility, like in the quasi-1D-regions near to the top and left boundaries of the structure) obtain an anomalous character by the transition to high injection mode near the pn-junction corner. An explanation for this "mobility anisotropy induced anomalous charge accumulation" is given on the basis of pn-junction and base voltage drops balance analysis in Fig.5. This theory assumes high injection in the N-base, spatially constant hole densities  $p_{||}, p_{\perp}$  and as a quite rough estimation, the equality of vertical and horizontal current densities. The last assumption is confirmed by flowline maps in Fig.4 and by the fact that the considered area dimensions are of the same range as the charge carrier diffusion lengths. Compared to the predicted  $p_{||}/p_{\perp}=2.8$ , Fig.4 gives a value  $\approx 1.5$ . This can be explained by the inequality of vertical and horizontal current densities (greater horizontal current is equivalent to smaller A in formulae of Fig.5).

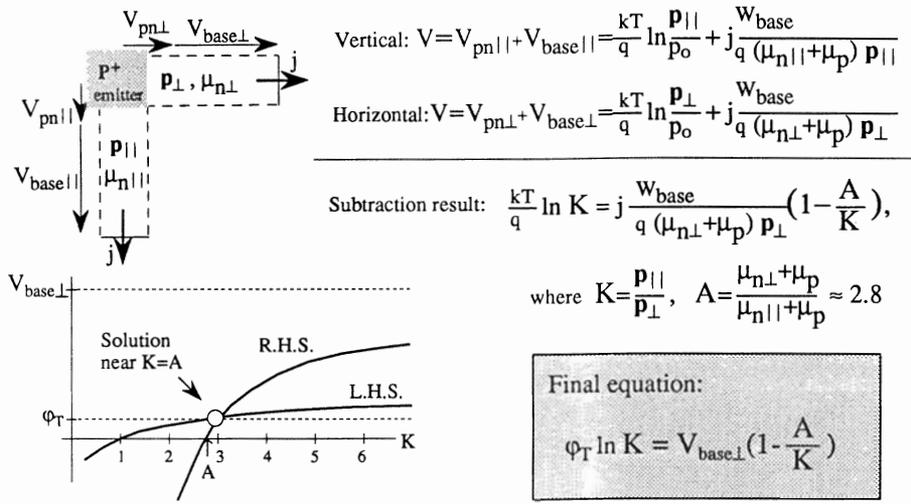


Fig.5 Theoretical explanation of the anomalous charge accumulation effect

### 5. Acknowledgement

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