DELAY TIME FOR GENERAL DISTRIBUTED NETWORKS WITH APPLICATION TO TIMING ANALYSIS OF DIGITAL MOS INTEGRATED CIRCUITS

Corneliu A. Marinov⁽¹⁾ and Pekka Neittaanmäki⁽²⁾

- (1) Faculty of Electrotechnics, Polytechnical Institute of Bucharest, Bucharest 77206, Romania
- (2) Department of Mathematics, University of Jyväskylä, Seminaarinkatu 15, SF-40100 Jyväskylä, Finland

SUMMARY

In this work we consider a general network with distributed parameter elements resistively coupled and additionally containing nonlinear capacitors. This network is a better model for the timing analysis of MOS integrated circuits than those previously considered. We define a global parameter " λ -delay time" which expresses the speed of signal propagation in this circuit. A computationally simple formula for an upper bound of this parameter is derived and compared with the bound obtained by numerical calculus.

1. INTRODUCTION

The major performance criterion for digital circuits is the speed of signal propagation. This performance is expressed by the "delay time", which is defined (and denoted) in many different ways by different authors, but having a single intuitive meaning: it is the time between the moment of changing the input variable and the moment when the associated output variable reaches a steady state value.

Of course, at various stages of MOS LSI design one has to verify this performance. This can be done by CAD circuit programs, so called "circuit simulators" as SPICE, ASTAP and SCEPTRE. This kind of programs are very slow for usual handling in the initial stages of design due to the use of complete models of components and especially due to computing method (the integration of differential equations describing the network). This is why faster programs so called "timing simulators" were developed, [1-9], which use simpler models of components and approximation and easily computable formulae for the delay time. In this respect, modelling MOS chips by RC networks has become a well accepted practice [1-6]. The transistor is usually replaced by a resistor, between drain and source, which has a pullup or a pulldown value in ON state and ∞ value in OFF state. Capacitances associated with the pullup source diffusion, contact cuts and the gates being driven are included, connecting respective nodes to the ground. In addition, transmission lines made of series resistors and shunt capacitors are models for With this connecting wires in MOS integrated chips. kind of models, the evolution of a MOS circuit is approximated by a sequence of RC networks corresponding to various states of each transistor.

It results from the above that the problem of estimating the delay of an MOS circuit reduces to that of an RC lumped network. For RC fan out ("RC trees") networks, easily computable bounds of the delay time associated with every node (the input being the same) are given in [1] and are incorporated into timing analysis programs [1,4,5]. The extension of these bounds to mesh networks was given in [10]. Also, in [2] computationally efficient formulae for the delay of any node were developed and included in a simulator. The present paper tries to improve the evaluations

of the delay time, beginning from the following remarks:

1. In all quoted papers the transistor interconnections are approximated by RC ladder networks. The accuracy of this approximation (from the delay time point of view) is not clear, being studied only for one interconnection line [12]. The problem is too important the advances in technology, to be neglected. as the integrated circuit chip size, complexity and device packing density are continuosly increasing (e.g, the prediction of a 0.5 µm feature size and 200 mm⁻ chip size by the late 80's appears to be reasonable [13]. As the minimum feature size is made smaller, the crosssection area of the interconnection also reduces. At the same time, a higher integration level allows the chip area to increase, causing the length of the interconnection to increase. That is why, for a very large chip with extremely small geometries, the delay time associated with interconnections becomes an appreciable part of the total time delay, and in certain cases dominates the chip performance [13]. Consequently an increased attention is focused either to modelling [14] or towards the technological advance [15] of the interconnections.

All these considerations justify our intention to consider more elaborate models for the connecting wires, described by telegraph equations. These equations take into account the distributed resistive capacitive character of the lines and the supplementary dielectric losses neglected in previous studies. Despite mathematical difficulties involved, this more exact modelling gives very easily computed final evaluations for the delay time.

2. Let us consider the example presented in Fig 1.1



Fig 1.1

Here an enhancement load nMOS bootstraped inverter drives the gates A and B through the three lines L_1 , L_2 , L_3 (implemented in polycrystalline silicon, metal silicide or metal).

In Fig 1.2 we have drawn a possible equivalent circuit for Fig 1.1 network (where the lines are represented by using the Ghausi symbol)



Most RC models considered in the quoted works have the capacitors exclusively connected between nodes and ground. As we see from the above example the capacitor C (boatstrap) does not fulfill this condition and so the Rubinstein evaluation [1], for example, does not hold. The same happens if we consider the Miller internodal capacitances. In fact the "floating" capacitors cause many difficulties in simulators [16]. Below we shall deal with a very general network, including the "RC tree" from [1.4.5] or the "RC meshes" from [2,10], (see Fig 2.1), which eliminates the above constraints.

3. The RC networks considered in [1,2,10,11] (with which our work is especially related) have a single

input source V (or V), and the authors evaluate a delay time associated to each node. Even the above example shows that it is possible that two inputs V_{in} and V_{DD} are simultaneously connected. The ability to handle this case rests on a clear definition of a global delay time, which expresses the rate of evolution of the whole network from the initial condition towards the steady state, when all (or a part of) inputs have step variations.

An asymptotic stability property of our general network allows us to obtain an upper bound of this delay time. The tightness of this bound is then verified by numerical calculus.

The last remark: the simplicity of the RC model of the transistors considered in all above quoted works gives obvious errors in the delay time of devices. Consequently, most authors consider linear RC models giving only the time associated with interconnections [1,10]. Of course many efforts must be made to unify the devices and the wiring delay. Our aim is to give computationally simple evaluations taking into consideration more realistic models for transistors and is therefore difficult to achieve. Yet it seems that some steps have been made, especially to include nonlinearities [3,11]. To the same goal we consider here the capacitors as nonlinear elements.

2. STATEMENT OF THE PROBLEM

Let us consider a linear resistive structure with 2n+m pairs of terminals, described by a constant matrix G of conductances and a vector B depending on sources. Because our interest is in step sources, the vector B may be supposed constant in time. Hence, we shall assume that the resistive multiport introduces the constraint

(2.1) i = -Gu + B,

where i and u are 2n+m vectors of terminals currents and voltages.

As we see in Fig 2.1., at first 2n terminals of the multiport are connected by n distributed parameter elements.



Fig 2.1

For each of these "r-c-g lines" we can write the wellknown telegraph equations:

(2.2)
$$\begin{cases} \frac{\partial u_{k}(t,x)}{\partial x} = -r_{k}i_{k}(t,x) \\ \frac{\partial i_{k}(t,x)}{\partial x} = c_{k}\frac{\partial u_{k}(t,x)}{\partial t} - g_{k}u_{k}(t,x) \\ k = 1,2,\dots n \end{cases}$$

Everywhere in the following we shall consider constant r_{k}, c_{k} and g_{k} parameters and $r_{k}, c_{k} > 0, g_{k} \geq 0$. Also, at last, m pairs of terminals are connected nonlinear capacitors, charge controlled by functions $f_{k}: \mathbb{R} \rightarrow \mathbb{R}$, $k = 1, \ldots m$.

From Fig 2.1 and by using (2.1) and (2.2) we easily derive the following mathematical model of our problem:

- a system of parabolic linear equations

(2.3)
$$\begin{cases} \frac{\partial u_k}{\partial t} = \frac{1}{r_k c_k} & \frac{\partial^2 u_k}{\partial x^2} - \frac{g_k}{c_k} u_k \\ x \in [0, d_k], t \ge 0, k = 1, \dots, n \end{cases}$$

- a set of nonlinear boundary conditions

$$(2.4) \begin{bmatrix} -\frac{1}{r_{1}} & \frac{\partial u_{1}}{\partial x} & (t,0) \\ \frac{1}{r_{1}} & \frac{\partial u_{1}}{\partial x} & (t,d_{1}) \\ \vdots \\ -\frac{1}{r_{n}} & \frac{\partial u_{n}}{\partial x} & (t,0) \\ \frac{1}{r_{n}} & \frac{\partial u_{n}}{\partial x} & (t,d_{n}) \\ \frac{dq_{1}}{dt} \\ \vdots \\ \frac{dq_{m}}{dt} \end{bmatrix} = -G \cdot \begin{bmatrix} u_{1}(t,0) \\ u_{1}(t,d_{1}) \\ \vdots \\ u_{n}(t,0) \\ u_{n}(t,d_{n}) \\ f_{1}(q_{1}) \\ \vdots \\ f_{m}(q_{m}) \end{bmatrix} +B$$

and, of course, the initial conditions

(2.5)
$$\begin{cases} u_k(0,x) = u_{k0}(x) , k = 1,...n \\ q_k(0) = q_0 , k = 1,...m. \end{cases}$$

The following assumptions will be taken into consideration:

A: The functions $f_k: \mathbb{R} \to \mathbb{R}$ $k = 1, \ldots, m$ are continuous and there exist strictly positive real numbers \underline{M}_k and \overline{M}_k such that for every x,y $\in \mathbb{R}$ we have

$$(2.6) \qquad \underline{M}_{k}|x-y| \leq |f_{k}(x) - f_{k}(y)| \leq \overline{M}_{k}|x-y|.$$

A₂: There exist positive constants $C_1, \ldots C_m$, such that for each i = 1,...2n we have

(2.7) -
$$G_{ii} + \sum_{j=1, j \neq i}^{2n} |G_{ij}| + \sum_{j=2n+1}^{2n+m} |G_{ij}| C_{j-2n} \overline{M}_{j-2n} < 0$$

and for each $i = 2n+1, \ldots, 2n+m$ it holds

(2.8)

$$\begin{array}{c}
 - G_{ii}M_{i-2n}C_{i-2n} + \sum_{j=1}^{2n} |G_{ij}| + \\
 2n+m \\
 + \sum_{j=2n+1, j \neq i}^{2n+m} |G_{ij}|M_{j-2n}C_{j-2n}| \\
\end{array}$$

In addition, we shall suppose that the initial conditions (2.5) are so smooth that our problem (2.3)+ +(2.4)+(2.5) has a unique solution in the classical sense, i.e. there are n functions $u_1:[0,\infty)\times[0,d_n] \rightarrow \mathbb{R}$, C in time and C in space, and m functions $q_1:[0,\infty) \rightarrow \mathbb{R}$, C in time, satisfying the above equations. Also, we shall suppose that there exist twice differentiable functions $u_1:[0,d_n] \rightarrow \mathbb{R}$, k = 1,...,n and the constants $q_1,...,q_m$ which satisfy the time independent (steady state) problem corresponding to (2.3)+(2.4)+(2.5). In the case m=0 we considered the existence and unicity problem in [17] and [18]. Some remarks about our hypotheses:

- The hypothesis A, allows us to consider a very large class of nonlinear characteristics of capacitors, among which the important engineering cases of continuous differentiable (with bounded derivatives) and piecewise continuous functions.

- The constants \tilde{M}_k and M_k in(2.6) have to the dimensions of [capacitance] while the constants C from (2.7) and (2.8) have to the dimensions of capacitance.

- The relations (2.7) and (2.8) seem to be cumbersome. In fact they are very natural because, in the case of 1 linear capacitors, we have $M_{k} = \overline{M}_{k} =$ [the capacitance] and if we choose $C_{k} =$ the capacitance, then these relations become

(2.9) $-G_{ii} + \sum_{j=1, j \neq i}^{2n+m} |G_{ij}| < 0$.

This condition is the very well known "diagonally rowsum dominant" property that can assure the existence and unicity [17], [18] and is frequently considered in circuit theory.

- As our examples and our experience show, the above hypotheses are not very restrictive, they are satisfied in most practical circuits cases.

3. THE TRANSIENT BEHAVIOR. DELAY TIME EVALUATION

The global behavior of our network can be described by a fuction $D:[0,\infty] \rightarrow \mathbb{R}$ named "delay" and defined by

The delay begins from 1 (corresponding to the initial conditions) and tends to 0 (corresponding to the steady state). If we choose $\lambda \in (0,1)$ then, the speed of this evolution can be expressed by the last moment when the delay passes through the λ value. In this way, we are conducted to define the " λ delay time" as

$$(3.2) \qquad \mathbb{T}_{\lambda} = \sup \left\{ t; D(t) = \lambda \right\} .$$

In the following we shall find an upper bound \overline{T}_{λ} for this T. We shall give our arguments only in outline. The reader can find the details in [19-23]. First of all a useful remark. By intuition we can say that the delay does not change if we invert the terminals of any line. This fact can be rigorously proved [22]. The initial notation of the terminals of the lines corresponds to the matrix G and to the sequence $\delta = (\delta_1, \ldots, \delta_n)$ where $\delta_1 = 0$ for all j. The inversion of the terminals of the k-th line will be marked by $\delta_k = 1$, and will correspond to a matrix $G^{\delta}=G^{(0,1,0,0)}$ which has the 2k-1-th and 2k-th rows interchanged as well as 2k-1-th and 2k-th columns. In [22] we have proved that the initial problem (2.3)+(2.4)+(2.5) and the similar problem constructed with G^{δ} , where $\delta = (\delta_1, \ldots, \delta_n)$ and $\delta_k = 0$ or 1, have the same delay. But the upper bound given below of the $\lambda \overline{d}$ delay time will depend on G^{δ} and will be denoted by $\overline{T}^{\delta}_{\lambda}$. It is clear now that we can take for \overline{T}_{λ} the minimum value of these upper bounds, namely

$$(3.3) \qquad \bar{T}_{\lambda} = \min_{\delta} \bar{T}_{\lambda}^{\delta} ,$$

where the minimum with respect to δ means the minimum with respect to all 2ⁿ cases appearing when we change the matrix G by the indicated manner. To find $\overline{T}_{\lambda}^{\delta}$ we shall use in the following the matrix G^{δ} instead of G. It is easily seen that the relations (2.7) and (2.8) are equivalent with the same relations written with the elements of G^{δ}.

$$(3.4) \qquad ||h|| = \max \max_{\substack{1 \le i \le n+m \\ i}} \max_{i} |h_i(x_i)|$$

We shall make a change of variables in our problem (2.3)+(2.4)+(2.5), namely

(3.5)
$$\begin{cases} u_{k}(t,x) = v_{k}(t,x)\cos\frac{a_{k}x}{d_{k}}, x \in [0, d_{k}], k=1, \dots n \\ q_{k}(t) = v_{k+n}(t) \cdot C_{k}, k=1, \dots m \end{cases}$$

where the constants α_{k} will be chosen below. Let us take the following subset of Z:

$$\mathcal{D}(A) = \{h \in \mathbb{Z}; h_1, h_2, \dots, h_n \text{ twice differentiable and} \}$$

$$\begin{bmatrix} -\frac{1}{r_{1}} \frac{\partial h_{1}(0)}{\partial x} \\ +\frac{1}{r_{1}} \frac{\partial h_{1}(d_{1})}{\partial x} \cos \alpha_{1} \\ \frac{1}{r_{n}} \frac{\partial h_{n}(0)}{\partial x} \\ +\frac{1}{r_{n}} \frac{\partial h_{n}(0)}{\partial x} \\ +\frac{1}{r_{n}} \frac{\partial h_{n}(d_{n})}{\partial x} \cos \alpha_{n} \end{bmatrix} = -\overline{G}^{\delta} \begin{bmatrix} h_{1}(0) \\ h_{1}(d_{1})\cos \alpha_{1} \\ h_{n}(0) \\ h_{n}(\alpha_{n})\cos \alpha_{n} \\ \frac{f_{1}(h_{n+1}C_{1})}{r_{n}(h_{n+m}C_{m})} \end{bmatrix} + \begin{bmatrix} 0 \\ \frac{\alpha_{1}h_{1}(d_{1})}{r_{1}d_{1}} \sin \alpha_{1} \\ \vdots \\ 0 \\ \frac{\alpha_{1}h_{1}(d_{1})}{r_{1}d_{1}} \sin \alpha_{1} \\ \frac{\partial h_{n}(d_{n})}{r_{n}d_{n}} \sin \alpha_{n} \end{bmatrix} + \overline{B}^{\delta} \} \cdot$$

Here we have denoted by \overline{G}^{δ} the $2n \times (2n \pm m)$ matrix extracted from upper part of G, and by \overline{B}° the vector with first (δ inverted) 2n elements from B. Let us take the operator $A: \mathcal{D}(A) \rightarrow Z$, defined by:

 $A\begin{pmatrix} \binom{n}{1} \\ \binom{n}{n+m} \end{pmatrix} (x_1, \dots, x_n) = an \ n+m \ vector \ with \ components:$ $\frac{1}{r_k c_k} \frac{\partial^2 h_k(x_k)}{\partial x_k^2} - (\frac{2}{r_k c_k} \frac{\alpha_k}{d_k} tg \frac{\alpha_k x_k}{d_k}) \frac{\partial h_k(x_k)}{\partial x_k} - (\frac{\alpha_k^2}{d_k^2 r_k c_k} + \frac{g_k}{c_k}) h_k(x_k)$ for $k = 1, \dots n$ and $-\frac{1}{c_{k-n}} \left[\sum_{j=1}^n G_{n+k}^{\delta}, 2j-1 h_j(0) + \sum_{j=1}^n G_{n+k}^{\delta}, 2j h_j(d_j) \cos \alpha_j + \sum_{j=1}^m G_{n+k}^{\delta}, 2n+j f_j(h_{n+j} c_j) \right] \quad \text{for } k = n+1, \dots, n+m.$

With these, we can easily show that our problem is equivalent with the following abstract Cauchy problem

$$(3.6) \begin{cases} \frac{d}{dt} v(t, \cdot) = Av(t, \cdot) + B^{*} \\ v(0, \cdot) = a \text{ function with components} \\ u_{k0}(\cdot)/\cos\alpha_{k} \frac{\cdot}{d_{k}} \text{ for } k = 1, \dots, n \text{ and} \\ q_{k-n}, 0^{/C} k-n & \text{ for } k = n+1, \dots, n+m \end{cases},$$

where B* is a vector with zero for the first n components and b_{2n+k}/C_k , $k = 1, \dots$ for the following ones. Let us further denote $(3.7) \ s_{i}^{\delta} = \begin{cases} 2n & 2n+m \\ \Sigma & |G_{ij}^{\delta}| + \sum_{j=2n+1}^{2n+m} |G_{ij}^{\delta}| C_{j-2n}^{\overline{M}}_{j-2n}, \text{ for } i=1, \dots \\ 2n & 2n+m \\ \Sigma & |G_{ij}^{\delta}| + \sum_{j=2n+1, j \neq i}^{2n+m} |G_{ij}^{\delta}| C_{j-2n}^{\overline{M}}_{j-2n}, \text{ for } i=2n+1, \\ \dots, 2n+m. \end{cases}$ The following lemma is from the mathematical point of view our basic result. <u>Lemma 3.1</u> Under the hypotheses A and A $2 \left[\begin{array}{c} \text{let } \gamma^{\circ}, \\ j = 1, \dots, n \end{array} \right]$ be the unique solution in $2 \left(0, \frac{\pi}{2} \right)^{\circ}$ of the equation: (3.8) $\cos\gamma_{j}^{\delta} = \left[s_{2j}^{\delta} + \sqrt{(s_{2j}^{\delta})^{2} + \frac{4}{r_{j}d_{j}}(g_{2j,2j}^{\delta} + \frac{1}{r_{j}d_{j}})}\right]/2(g_{2j,2j}^{\delta} + \frac{1}{r_{j}d_{j}})$ If we take $(3.9) \quad \omega_{\varepsilon}^{\delta} = \max\left\{\max_{1 \le j \le n} \left[-\frac{(\gamma_{j}^{\delta} - \varepsilon)^{2}}{d_{z}^{2}r_{z}c_{z}} - \frac{g_{j}}{c_{j}}\right]; \max_{n+1 \le j \le n+m} \left(\frac{-G_{n+j,n+j}^{\delta} + S_{n+j}^{\delta}}{c_{j-n}}\right)\right\}$ then, the operator $A - \omega_e^{\delta}I$, constructed with $\alpha_j = \gamma_j^{\delta-\varepsilon} \in (0, \frac{\pi}{2})$. is dissipative in the space $\pi \square$ For the proof we refer to [21,23]. <u>Theorem 3.1.</u> If A_1 and A_2 hold, then for all $t \ge 0$ $D(t) \le e^{-\omega_0^{\delta}t}$ and then $1 \le i \le n$ (3.10) $\overline{T}_{\lambda}^{\delta} = \frac{\ln(\lambda \min_{1 \le i \le n} \cos \gamma_{i}^{\delta})}{\omega^{\delta}}$ This theorem gives a global asymptotic stability property for the steady state solution, i.e. the dynamic state tends to the same steady state for any initial conditions.

For the sake of clarity we shall resume the procedure to obtain \overline{T} , namely in the linear case where the writing is shorter. <u>Procedure 3.1</u> (for computing \overline{T}) <u>Step 1.</u> Given the matrix G. Compute for each i = 1,.

Step 1. Given the matrix G. Compute for each i = 1, ..., 2n+m, the size 2n+m $S_i = \sum |G_i|$ k=1 $k \neq j$ <u>Step 2.</u> For each $j = 1, \ldots, n$ calculate

(3.11)
$$\cos \gamma_{j}^{\delta} = S_{2j} + \sqrt{S_{2j}^{2} + \frac{4}{r_{j}d_{j}}} (G_{2j,2j} + \frac{1}{r_{j}d_{j}})/2(G_{2j,2j} + \frac{1}{r_{j}d_{j}})$$

or
 $S_{2j-1} + \sqrt{S_{2j-1}^{2} + \frac{4}{r_{j}d_{j}}} (G_{2j-1,2j-1} + \frac{1}{r_{j}d_{j}})/2(G_{2j-1,2j-1} + \frac{1}{r_{j}d_{j}})$

Step 3. For γ_j^{δ} from above, with $G_{n+j,n+j}^{\circ} = G_{n+j,n+j}$ and $S_{n+j}^{\delta} = S_{n+j}$ for $j=n+1,\ldots,n+m$ and with $\varepsilon=0$ compute the 2ⁿ values of ω_0^{δ} from (3.9) Step 4. The relations (3.10) and (3.3) give the desired value of \overline{T}_1 .

As we see we found a very simple and fast method to compute the upper bound of the λ -delay time.

4. THE EXAMPLES

The following example is given for the purpose to verify the tightness of T_1 to T_1 .

Let us consider the fan-out circuit from Fig 4.1, with two inputs $E_{\pm} = E_{\pm} = 1v$, with lumped elements having values: $R_{\pm} = \frac{1}{2}\Omega$, $R_{\pm} = \frac{1}{2}\Omega^2$, $C_{\pm} = 3F$, $C_{\pm} = \frac{3}{2}F$, $C_{\pm} = 3F$, and with four r-c-g lines.



The lines have the parameters: $r_{d} = 1\Omega$ for $i=1, \ldots, 4$, $c_{1}d_{1}=3/2F$, $c_{2}d_{2}=1F$, $c_{3}d_{3}=5/4F$, $c_{1}d_{1}=4/3F$. This results $a \ 12 \times 12$ G matrix whose nonzero elements are: $G_{11}=2$, $G_{22}=G_{33}=G_{99}=3/2$, $G_{44}=G_{55}=G_{77}=G_{10,10}=14/9$, $G_{66}=G_{88}=G_{11,11}=G_{12,12}=6/5$, and the nondiagonal ones: $G_{23}=G_{32}=G_{29}=G_{92}=G_{39}=G_{93}=-1/2$, $G_{45}=G_{54}=G_{47}=G_{74}=G_{4,10}=G_{10,4}=G_{57}=G_{75}=G_{5,10}=G_{10,5}=G_{7,10}=G_{10,7}=-4/9$, $G_{6,12}=G_{12,6}=G_{8,11}=G_{11,8}=-4/5$. The nonzero elements of B are $b_{1}=2$, $b_{2}=b_{3}=b_{0}=1/2$.

The following table summarizes the calculus for delay time in Procedure 3.1.

Table	4.1						
δ	cosy ₁	cosy ₂	cosy3	cosyy	ω ^δ Ο	Ŧ _{0.1}	Ŧ 0.5
0000	0.863	0.938	0.880	0.880	-0.124	19.75	6.78
1000	0.577	0.938	0.880	0.880	-1.124	23.00	10.02
0100	0.863	0.863	0.880	0.880	-0.133	18.42	6.32
0010	0.863	0.938	0.938	0.880	-0.099	24.74	8.49
0001	0.863	0.938	0.880	0.938	-0.0929	26.37	9.04
1100	0.577	0.863	0.880	0.880	-0.133	21.45	9.35
0110	0.863	0.863	0.938	0.880	-0.099	24.74	8.49
0011	0.863	0.938	0.938	0.938	-0.0929	.26.37	9.04
1010	0.577	0.938	0.938	0.880	-0.124	23.00	10.02
1001	0.577	0.938	0.880	0.938	-0.0929	30.70	13.38

0.880

0.938

0.880

0.938

0.938

0.938

0.863

0.863

0.863

0.863

0.938

0.863

0101 0.863

1110 0.577

1101 0.577

0111 0.863

1011 0.577

1111 0.577

As we see, $\overline{T}_{0,1} = 18.42$ s and $\overline{T}_{0,5} = 6.32$ s. The values of delay time numerically computed (Crank - Nicolson method in time and finite element method in_space (linear elements) - see detail in [22]) are $\overline{T}_{0,1}$ =7.1 s and $T_{0,5}$ =2.11.(see Fig 4.4).

0.938

0.880

0.938

0.938

0.938

0.938

Figure 4.2 shows the approximation for u.(t,.) for different time moments t_i (indicated by -) as well as the steady state solutions $u_i(\cdot)$ i=1,...,4 (indicated by ---)



Fig 4.2

In Fig 4.3 we see approximation for u; (t), i=5,...,8 for $t \in (0, 40)$.

28.81

-0.0929 30.70 13.38

-0.0929 30.70 13.38

-0.0929 30.70 13.38

-0.0929 26.37

-0.0929 26.37

-0.099

9.04

12.55

9.04



Figure 4.4 shows the a-priori upper bound for the delay time (obtained by Theorem 3.1, $\delta = (0, 1, 0, 0)$) as well the delay time obtained from the discrete model (computed delay).



Fig 4.4

For further numerical examples (linear and nonlinear cases) we refer to [17 - 23]. The authors are indebted to Mr. V. Hara for his assistance in numerical tests.

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REFERENCES

[1]	RUBINSTEIN, J and PENFIELD, P, Jr, and HOROWITZ,
	"Signal delay in RC tree nerworks", IEEE Trans.
	Computer-Aided Design, vol CAD-2, no 3, p.202,
	July 1983.
[2]	LIN, TM and MEAD, C.A.
	"Signal delay in general RC networks", IEEE Trans.
	Computer-Aided Design, vol CAD-3, no ⁴ , Oct. 1984.
[3]	HOROWITZ, M.A.
	"Timing models for MOS pass networks", in Proc.
	<u>Int.Symp.Circuits & Systems</u> , p.198, 1983.
C43	JOUPPI, N.P.
	"TV: An nMOS timing analyzer", in <u>Proc. 3rd</u>
_	Caltech Conf. VLSI, p.71, March 1983.
[5]	TAMURA, E, OGAWA, K. and NAKANO, T.
	"Path delay analysis for hierarchical building
	block layout system" in <u>ACM IEEE 20th Design</u>
	Autom.Conf.Proc., p.403, June 1903.
[0]	BRYANT, R.E.
	"A switch-level model and simulator for Mos
	algital systems", <u>IEEE Trans.computers</u> , voi: 0 55,
F77	D. 100, Feb. 1904.
L J	"Autodology, A gooond generation automatic delay
	calculation program for LSI/VLSI chips", in Proc.
	TEEE Int. Conf. Computer-Aided Design, pp. 188-190,
	Nov. 1984
[8]	TERMAN, C.J.
	"RSIM - A logic-level timing simulator", in Proc.
	IEEE Int.Conf. on Computer Design: VLSI in
	Computers, p.437, Oct. 1983.
[9]	OUSTERHUT, J.
	"CRYSTAL: A timing analyzer for NMOS VLSI circuits",
	in Proc. 3rd Caltech Conf. on VLSI, p.57, March
_	1983.
[10]	WYATT, J.L., Jr.
	"Signal delay in RC mesh networks", <u>IEEE Trans.</u>
	Circuits & Systems, Vol. CAS-32, no 5, p.507,
	WIATT, J.L. Jr. "Menators consitinity of realizon nonuniform PC
	the transmission lines, with explication to timing
	englysis digital MOS integrated circuits" TEEE
	Trans Circuits & Systems Vol CAS-32, no 1 Jan.
	1985.
Г 12 Т	SAKURAT. T.
	"Approximation of wiring delay in MOSFET LSI".
	IEEE J.Solid-State Circuits, Vol SC-18 no 4.
	Aug. 1983.
[13]	SARASWAT K.C. and MOHAMMADI, F.
	"Effect of scaling of interconnections on the

time delay of VLSI circuits", <u>IEEE Trans.Electr</u>. <u>Dev</u>., Vol ED-29, no 4, April 1982.

- [14] SCOTT, D.B., HUNTER, W.R. and SHICHIJO, H. "A transmission line model for silicided diffusions: impact on the performance of VLSI circuits", <u>IEEE Trans.Electrn.Devices</u>, Vol ED-29, no 4, April 1982.
- [15] GARDNER, D.S., MICHALKA, T.L. SARASWAT, K.C. BARBEE, T.W., Jr. MCVITTIE, J.P. and MEINDEL, J.D. "Layered and homogenous films of aluminium and aluminium/silicon with titanium and tungsten for multilevel interconnects", <u>IEEE J. Solid State</u> Circuit. Vol SC-20, no 1, Feb. 1985.
- [16] DE MICHELI, G. NEWTON, A.R. and SANGIOVANNI-VINCENTELLI, A. "Symmetric displacement algorithms for the timing analysis of large scale circuits", <u>IEEE Trans.</u> <u>Computer Aided Design</u>, Vol CAD-2, No 3, July 1983.
- [17] MARINOV, C.A. NEITTAANMÄKI, P. "The problem of response time in electrical circuits with resistively coupled distributed structures", Res.Rep. 8-1985, Lappeenranta Univ. of Tech.
- [18] MARINOV, C.A., NEITTAANMÄKI, P. "A theory of electrical circuits with resistively coupled distributed structures. Delay time predicting", Res.Rep. 21-1985, Lappeenranta Univ. of Tech.
- [19] NEITTAANMÄKI, P., KOIKKALAINEN, P. and MARINOV, C.A. "On the computation of the delay time in some parabolic problems", in <u>Proc.Summer School in</u> <u>Numerical Anal.(Ed. by P. Neittaanmäki), Bericht</u> <u>31, Univ. of Jyväskylä, p.239, 1985</u>
- [20] MARINOV, C.A., NEITTAANMÄKI, P. "Delay time predicting for distributed parameter circuits", <u>Proc.6th Int.Conf.Contr.Syst.Comp.Sci</u>., p.100, Bucharest, May 1985.
- [21] MARINOV, C.A., NEITTAANMÄKI, P. "Asymptotic stability of a class of parabolic equations arising in circuit theory". Preprint 43, Dept.Math.Univ.Jyväskylä 1986.
- [22] MARINOV, C.A., NEITTAANMÄKI, P. "On bounds evaluation and numerical approach in delay time problem", Res.Rep. 8-1986, Lappeenranta Univ. of Tech.
- [23] MARINOV, C.A., NEITTAANMÄKI, P. "Asymptotic bahavior for a parabolic problem with nonlinear boundary conditions", Res.Rep. 9-1986, Lappeenranta Univ. Tech.