Schrödinger/Luttinger Approach to Scaled MOS Transport for Various Crystal Orientations and its Experimental Verifications

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INTRODUCTION

In scaled device designing for the 40 nm regime and beyond, deeper physical insight into channel transport will be required than previously. A Schrödinger/Luttinger approach to scaled MOS transport was applied for the first time to various crystal orientations, channel directions, applied voltages and mechanical stresses together with experimental verifications. Typical findings obtained in this work are as follows: (1)marked variations of inversion carrier distributions have been calculated among (110), (111), (112), (120) and (100) surfaces, (2) significant differences in channel conductivities of p-channel MOS have been simulated between triode and pentode regions, and (3)effective mass characteristics have been output as functions of germanium concentration.

SCHRÖDINGER/LUTTINGER BASED APPROACHES

Newly developed Schrödinger/Luttinger prototype codes are composed of (1)extended Luttinger $^{[1][2][3]}$ equations with stress/strain tensor and matrix solvers, (2)Poisson solver^[4], and (3)Monte Carlo parts^[5] with anisotropic subband energy and scattering rates linked with each other as shown in Figs.1 and 2. Typical subband structures for p-channel inversion layers, band diagrams of (110) Si and inversion charge distribution are presented in Figs.3, 4 and 5, respectively. It should be noted that distinctive variations of hole inversion band scheme were calculated for the first time among (110), (111), (120), and (100) surfaces in terms of external stress, and significant differences in channel carriers of p-channel MOS have been simulated among Si Si/Ge and Ge substrates. Figure 6 shows energy dependence of scattering rate results for carrier transports. Real-time snapshots of carrier distributions and drift mobility are demonstrated in Figs.7, 8 and 9. From the above results, it is first clarified quantitatively that (1)stress sensitivities of effective masses in Si/Ge and Ge substrate have been simulated and (2)the mobility advantage of [110] hole carriers on strained (100)Si is simulated to be smeared in high electric field. In order to verify the modes and simulated results, MOS cantilever experiment have been conducted as shown in Fig.10. Drain currents were measured upon application of external uniaxial stress. The amounts of additional strain $\Delta \varepsilon_{ij}$ were calculated by means of the following equation:

$$\Delta \varepsilon_{ii} = (3Z \times b \times \Delta h) / L^3,$$

where Δh , Z and L are indicated in Fig.10 and b is a half of the wafer thickness. Δh was measured using a micrometer head. In the case of [110] Si p-channel on (100) Si, measurement reveals that drain current gain decreases with compressive strain $\Delta \varepsilon_{ij}$ in the pentode region contrarily to the triode region, which is consistent with the results in Fig3.

SUMMARY

A Schrödinger/Luttinger approach to scaled MOS transport was applied for the first time to various kinds of crystal orientations, channel directions, applied voltages and mechanical stress together with experimental verifications. Through intensive analysis, it has been determined that Si/Ge and Ge are promising materials from the view-point of scaled CMOS drivability in conjunction with mechanical stress and crystal orientations. Details of mathematical derivations in Schrödinger/Luttinger part and scattering rate procedures will also be reported with calling upon current references ^{[6][7]} at the IWCE conference.

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subband in Ge and $Si_{1-x}Ge_x$ as a function of biaxial stress and material composition.

Fig.10 MOS cantilever experimental setup(a) and experimental results(b)(c). Dimensions of the specimen are as followed; L=39.08mm, Z=9.60mm and 2b=0.70mm. S/D is located on transversal to the beam, and generated strain is compressive on the S/D direction.